

LTspice IV Library API [Ver 4.11r]

[bi.asy](#)

spice_model: I=F(...)

Required Refdes Prefix: B

Description: Arbitrary behavioral current source

Spice_Node_Sequence: [1] + , [2] - ,

[bi2.asy](#)

spice_model: I=F(...)

Required Refdes Prefix: B

Description: Arbitrary behavioral current source

Spice_Node_Sequence: [1] + , [2] - ,

[bv.asy](#)

spice_model: V=F(...)

Required Refdes Prefix: B

Description: Arbitrary behavioral voltage source

Spice_Node_Sequence: [1] + , [2] - ,

[cap.asy](#)

spice_model: C

Required Refdes Prefix: C

Description: Capacitor

Spice_Node_Sequence: [1] A , [2] B ,

[csw.asy](#)

spice_model: CSW

Required Refdes Prefix: W

Description: Current controlled switch

Spice_Node_Sequence: [1] + , [2] - ,

[current.asy](#)

spice_model: I

Required Refdes Prefix: I

Description: Current source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] + , [2] - ,

[diode.asy](#)

spice_model: D

Required Refdes Prefix: D

Description: Diode

Spice_Node_Sequence: [1] + , [2] - ,

[e.asy](#)

spice_model: E

Required Refdes Prefix: E

Description: Voltage dependent voltage source

Spice_Node_Sequence: [1] + , [2] - , [3] P , [4] N ,

[e2.asy](#)

spice_model: E

Required Refdes Prefix: E

Description: Voltage dependent voltage source

Spice_Node_Sequence: [1] + , [2] - , [3] P , [4] N ,

[f.asy](#)

spice_model: F

Required Refdes Prefix: F

Description: Linear current dependent current source

Spice_Node_Sequence: [1] + , [2] - ,

[FerriteBead.asy](#)

Required Refdes Prefix: L_Ferrite_Bead

Description: A Ferrite Bead

Spice_Node_Sequence: [1] A , [2] B ,

[FerriteBead2.asy](#)

Required Refdes Prefix: L_Ferrite_Bead

Description: A Ferrite Bead(Alternate symbol)

Spice_Node_Sequence: [1] A , [2] B ,

[g.asy](#)

spice_model: G

Required Refdes Prefix: G

Description: Voltage dependent current source

Spice_Node_Sequence: [1] + , [2] - , [3] NC+ , [4] NC- ,

[g2.asy](#)

spice_model: G

Required Refdes Prefix: G

Description: Voltage dependent current source

Spice_Node_Sequence: [1] + , [2] - , [3] NC+ , [4] NC- ,

[h.asy](#)

spice_model: H

Required Refdes Prefix: H

Description: Linear current dependent voltage source

Spice_Node_Sequence: [1] + , [2] - ,

[ind.asy](#)

spice_model: L

Required Refdes Prefix: L

Description: Inductor

Spice_Node_Sequence: [1] A , [2] B ,

[ind2.asy](#)

spice_model: L

Required Refdes Prefix: L

Description: Inductor

Spice_Node_Sequence: [1] A , [2] B ,

[LED.asy](#)

spice_model: D

Required Refdes Prefix: D

Description: Light Emitting Diode

Spice_Node_Sequence: [1] + , [2] - ,

[load.asy](#)

spice_model: I

Required Refdes Prefix: I

Description: Current Source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] A , [2] B ,

[load2.asy](#)

spice_model: I

Required Refdes Prefix: I

Description: Current Source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] + , [2] - ,

[lpnp.asy](#)

spice_model: LPNP

Required Refdes Prefix: QP

Description: Bipolar Lateral PNP transistor. You would normally use this with your own .model statement.

Spice_Node_Sequence: [1] C , [2] B , [3] E , [4] S ,
[ltline.asy](#)

spice_model: LTRA

Required Refdes Prefix: O

Description: Lossy Transmission Line. Note: You must supply the .model for this device

Spice_Node_Sequence: [1] I1 , [2] R1 , [3] I2 , [4] R2 ,
[mesfet.asy](#)

spice_model: NMF

Required Refdes Prefix: Z

Description: GaAs MESFET transistor Note: You must supply the .model for this device

Spice_Node_Sequence: [1] D , [2] G , [3] S ,
[njf.asy](#)

spice_model: NJF

Required Refdes Prefix: JN

Description: N-Channel JFET

Spice_Node_Sequence: [1] D , [2] G , [3] S ,
[nmos.asy](#)

spice_model: NMOS

Required Refdes Prefix: MN

Description: N-Channel MOSFET transistor

Spice_Node_Sequence: [1] D , [2] G , [3] S ,
[nmos4.asy](#)

spice_model: NMOS

Required Refdes Prefix: MN

Description: N-Channel MOSFET transistor with explicit substrate connection(used for monolithic MOSFETS)

Spice_Node_Sequence: [1] D , [2] G , [3] S , [4] B ,
[nnp.asy](#)

spice_model: NPN

Required Refdes Prefix: QN

Description: Bipolar NPN transistor

Spice_Node_Sequence: [1] C , [2] B , [3] E ,
[nnp2.asy](#)

spice_model: NPN

Required Refdes Prefix: QN

Description: Bipolar NPN transistor

Spice_Node_Sequence: [1] C , [2] B , [3] E ,
[nnp3.asy](#)

spice_model: NPN

Required Refdes Prefix: QN

Description: Bipolar NPN transistor

Spice_Node_Sequence: [1] C , [2] B , [3] E ,
[nnp4.asy](#)

spice_model: NPN

Required Refdes Prefix: QN

Description: Bipolar NPN transistor with substrate node. You would normally use this with your own .model statement.

Spice_Node_Sequence: [1] C , [2] B , [3] E , [4] S ,
[npj.asy](#)

spice_model: PJF

Required Refdes Prefix: JP

Description: P-Channel JFET transistor

Spice_Node_Sequence: [1] D , [2] G , [3] S ,

[pmos.asy](#)

spice_model: PMOS

Required Refdes Prefix: MP

Description: P-Channel MOSFET transistor

Spice_Node_Sequence: [1] D , [2] G , [3] S ,

[pmos4.asy](#)

spice_model: PMOS

Required Refdes Prefix: MP

Description: P-Channel MOSFET transistor with explicit substrate connection(used for monolithic MOSFETS)

Spice_Node_Sequence: [1] D , [2] G , [3] S , [4] B ,

[pnp.asy](#)

spice_model: PNP

Required Refdes Prefix: QP

Description: Bipolar PNP transistor

Spice_Node_Sequence: [1] C , [2] B , [3] E ,

[pnp2.asy](#)

spice_model: PNP

Required Refdes Prefix: QP

Description: Bipolar PNP transistor

Spice_Node_Sequence: [1] C , [2] B , [3] E ,

[pnp4.asy](#)

spice_model: PNP

Required Refdes Prefix: QP

Description: Bipolar PNP transistor with substrate node. You would normally use this with your own .model statement.

Spice_Node_Sequence: [1] C , [2] B , [3] E , [4] S ,

[polcap.asy](#)

spice_model: C

Required Refdes Prefix: C

Description: Polarized Capacitor

Spice_Node_Sequence: [1] A , [2] B ,

[res.asy](#)

spice_model: R

Required Refdes Prefix: R

Description: A resistor

Spice_Node_Sequence: [1] A , [2] B ,

[res2.asy](#)

spice_model: R

Required Refdes Prefix: R

Description: A resistor

Spice_Node_Sequence: [1] A , [2] B ,

[schottky.asy](#)

spice_model: D

Required Refdes Prefix: D

Description: Schottky diode

Spice_Node_Sequence: [1] + , [2] - ,

[sw.asy](#)

spice_model: SW

Required Refdes Prefix: S

Description: Voltage controlled switch

Spice_Node_Sequence: [1] A , [2] B , [3] NC+ , [4] NC- ,

[tline.asy](#)

spice_model: Td=50n Z0=50

Required Refdes Prefix: T

Description: Ideal Lossless Transmission Line

Spice_Node_Sequence: [1] I1 , [2] R1 , [3] I2 , [4] R2 ,

[varactor.asy](#)

spice_model: D

Required Refdes Prefix: D

Description: Diode

Spice_Node_Sequence: [1] + , [2] - ,

[voltage.asy](#)

spice_model: V

Required Refdes Prefix: V

Description: Voltage Source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] + , [2] - ,

[zener.asy](#)

spice_model: D

Required Refdes Prefix: D

Description: Zener Diode

Spice_Node_Sequence: [1] + , [2] - ,

[LT1011.asy](#)

spice_model: LT1011

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Voltage Comparator

Spice_Node_Sequence: [1] GND , [2] In+ , [3] In- , [4] V- , [5] BAL , [6] STROBE , [7] OUT , [8] V+ ,

[LT1015.asy](#)

spice_model: LT1015

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: High Speed Dual Line Receiver(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] VCC , [4] VEE , [5] OUT+ , [6] LATCH ,

[LT1016.asy](#)

spice_model: LT1016

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra Fast Precision Comparator

Spice_Node_Sequence: [1] V+ , [2] In+ , [3] In- , [4] V- , [5] LATCH , [6] GND , [7] OUT+ , [8] OUT- ,

[LT1017.asy](#)

spice_model: LT1017

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: μ Power Dual Comparator(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1018.asy](#)

spice_model: LT1018

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: μ Power Dual Comparator(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1116.asy](#)

spice_model: LT1116

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 12ns, Single Supply Ground-Sensing Comparator

Spice_Node_Sequence: [1] V+ , [2] In+ , [3] In- , [4] V- , [5] LATCH , [6] GND , [7] OUT+ , [8] OUT- ,

[LT1394.asy](#)

spice_model: LT1394

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 7ns, Low Power Single Supply, Ground-Sensing Comparator

Spice_Node_Sequence: [1] V+ , [2] In+ , [3] In- , [4] V- , [5] LE , [6] GND , [7] Q , [8] _Q ,

[LT1671.asy](#)

spice_model: LT1671

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 60ns, Low Power, Single Supply, Ground-Sensing Comparator

Spice_Node_Sequence: [1] V+ , [2] In+ , [3] In- , [4] V- , [5] LE , [6] GND , [7] Q , [8] _Q ,

[LT1711.asy](#)

spice_model: LT1711

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: 4.5ns, 3V/5V/±5V Rail-to-Rail Comparator

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] GND , [6] OUT+ , [7] OUT- , [8] LATCH ,

[LT1712.asy](#)

spice_model: LT1712

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Dual 4.5ns, 3V/5V/±5V Rail-to-Rail Comparator(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] GND , [6] OUT+ , [7] OUT- , [8] LATCH ,

[LT1713.asy](#)

spice_model: LT1713

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: 7ns, Low Power, 3V/5V/±5V Rail-to-Rail Comparator

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] GND , [6] OUT+ , [7] OUT- , [8] LATCH ,

[LT1714.asy](#)

spice_model: LT1714

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Dual 7ns, Low Power, 3V/5V/±5V Rail-to-Rail Comparator(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] GND , [6] OUT+ , [7] OUT- , [8] LATCH ,

[LT1715.asy](#)

spice_model: LT1715

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: 7ns, Low Power, 3V/5V/±5V Rail-to-Rail Comparator

Spice_Node_Sequence: [1] In+ , [2] In- , [3] Vcc , [4] Vee , [5] VS+ , [6] GND , [7] OUT ,

[LT1716.asy](#)

spice_model: LT1716

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: SOT-23, 44V, Over-the-Top, μ Power, Precision, Rail-to-Rail Comparator

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1719.asy](#)

spice_model: LT1719

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 4.5ns, Single Supply 3V/5V Comparator with Rail-to-Rail Outputs

Spice_Node_Sequence: [1] V+ , [2] In+ , [3] In- , [4] V- , [5] GND , [6] SHDN , [7] OUT , [8] VS+ ,

[LT1720.asy](#)

spice_model: LT1720

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 4.5ns, Single Supply 3V/5V Comparator with Rail-to-Rail Outputs

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1721.asy](#)

spice_model: LT1721

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 4.5ns, Single Supply 3V/5V Comparator with Rail-to-Rail Outputs

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6700-1.asy](#)

spice_model: LT6700-1

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: μ Power, Low Voltage, SOT-23, Dual Comparator with 400mV Reference

Spice_Node_Sequence: [1] OUTA , [2] GND , [3] +INA , [4] -INB , [5] Vs , [6] OUTB ,

[LT6700-2.asy](#)

spice_model: LT6700-2

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: μ Power, Low Voltage, SOT-23, Dual Comparator with 400mV Reference

Spice_Node_Sequence: [1] OUTA , [2] GND , [3] -INA , [4] -INB , [5] Vs , [6] OUTB ,

[LT6700-3.asy](#)

spice_model: LT6700-3

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: μ Power, Low Voltage, SOT-23, Dual Comparator with 400mV Reference

Spice_Node_Sequence: [1] OUTA , [2] GND , [3] +INA , [4] +INB , [5] Vs , [6] OUTB ,

[LT6703-2.asy](#)

spice_model: LT6703-2

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: μ Power, Low Voltage, SOT-23, Dual Comparator with 400mV Reference

Spice_Node_Sequence: [1] OUT , [2] GND , [3] -IN , [4] Vs ,

[LT6703-3.asy](#)

spice_model: LT6703-3

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: μ Power, Low Voltage, SOT-23, Dual Comparator with 400mV Reference

Spice_Node_Sequence: [1] OUT , [2] GND , [3] +IN , [4] Vs ,

[LTC1041.asy](#)

spice_model: LTC1041

Required Refdes Prefix: X

spice_lib: LTC1041.sub

Description: BANG-BANG Controller

Spice_Node_Sequence: [1] ON/_OFF , [2] Vin , [3] SetPnt , [4] GND , [5] Delta , [6] OSC , [7] Vpp , [8] Vcc ,

[LTC1042.asy](#)

spice_model: LTC1042

Required Refdes Prefix: X

spice_lib: LTC1042.sub

Description: Window Comparator

Spice_Node_Sequence: [1] Within , [2] Center , [3] In , [4] GND , [5] Width/2 , [6] Above , [7] OSC , [8] Vcc ,

[LTC1440.asy](#)

spice_model: LTC1440

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultralow Power Comparator with Reference

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] GND , [7] REF , [8] HYST ,

[LTC1441.asy](#)

spice_model: LTC1441

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Ultralow Power Comparator with Reference

Spice_Node_Sequence: [1] OUT , [2] V- , [3] In+ , [4] In- , [5] V+ ,

[LTC1442.asy](#)

spice_model: LTC1442

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Ultralow Power Comparator with Reference

Spice_Node_Sequence: [1] OUT A , [2] V- , [3] InA+ , [4] InB- , [5] HYST , [6] Ref , [7] V+ , [8] OUT B ,

[LTC1443.asy](#)

spice_model: LTC1443

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Ultralow Power Comparator with Reference

Spice_Node_Sequence: [1] OUT , [2] V+ , [3] In- , [4] In+ , [5] REF , [6] V- , [7] GND ,

[LTC1444.asy](#)

spice_model: LTC1444

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Ultralow Power Comparator with Reference

Spice_Node_Sequence: [1] OUT , [2] V+ , [3] In- , [4] In+ , [5] REF , [6] V- , [7] HYST ,

[LTC1445.asy](#)

spice_model: LTC1445

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Ultralow Power Comparator with Reference

Spice_Node_Sequence: [1] OUT , [2] V+ , [3] In- , [4] In+ , [5] REF , [6] V- , [7] HYST ,

[LTC1540.asy](#)

spice_model: LTC1540

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Nanopower Comparator with Reference

Spice_Node_Sequence: [1] GND , [2] V- , [3] In+ , [4] In- , [5] HYST , [6] REF , [7] V+ , [8] OUT ,

[LTC1841.asy](#)

spice_model: LTC1841

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Ultralow Power Dual Comparator(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LTC1842.asy](#)

spice_model: LTC1842

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Ultralow Power Dual Comparator with Reference

Spice_Node_Sequence: [1] OUTA , [2] GND , [3] INA+ , [4] INB+ , [5] Hyst , [6] Ref , [7] Vcc , [8] OUTB ,

[LTC1843.asy](#)

spice_model: LTC1843

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Ultralow Power Dual Comparator with Reference

Spice_Node_Sequence: [1] OUTA , [2] GND , [3] INA+ , [4] INB- , [5] Hyst , [6] Ref , [7] Vcc , [8] OUTB ,

[and.asy](#)

Required Refdes Prefix: A

spice_lib: AND

Description: Behavioral AND gate

Spice_Node_Sequence: [1] a , [2] b , [3] c , [4] d , [5] e , [6] _Q , [7] Q , [8] com ,

[buf.asy](#)

Required Refdes Prefix: A

spice_lib: BUF

Description: Behavioral buffer with complementary outputs

Spice_Node_Sequence: [1] in , [2] _Q , [3] Q , [4] com ,

[buf1.asy](#)

Required Refdes Prefix: A

spice_lib: BUF

Description: Behavioral buffer

Spice_Node_Sequence: [1] in , [2] Q , [3] com ,

[dflop.asy](#)

Required Refdes Prefix: A

spice_lib: DFLOP

Description: Behavioral D-flipflop

Spice_Node_Sequence: [1] D , [2] CLK , [3] PRE , [4] CLR , [5] _Q , [6] Q , [7] com ,

[diffschmitt.asy](#)

Required Refdes Prefix: A

spice_lib: SCHMITT

Description: Behavioral Schmitt-Triggered Buffer with Complementary Outs and Differential Input

Spice_Node_Sequence: [1] in+ , [2] in- , [3] _Q , [4] Q , [5] com ,

[diffschmtbuf.asy](#)

Required Refdes Prefix: A

spice_lib: SCHMITT

Description: Behavioral Schmitt-Triggered Buffer with Differential Input

Spice_Node_Sequence: [1] in+ , [2] in- , [3] Q , [4] com ,

[diffschmtinv.asy](#)

Required Refdes Prefix: A

spice_lib: SCHMITT

Description: Behavioral Schmitt-Triggered Inverter with Differential Input

Spice_Node_Sequence: [1] in+ , [2] in- , [3] _Q , [4] com ,

[inv.asy](#)

Required Refdes Prefix: A

spice_lib: BUF

Description: Behavioral Inverter

Spice_Node_Sequence: [1] in , [2] _Q , [3] com ,

[or.asy](#)

Required Refdes Prefix: A

spice_lib: OR

Description: Behavioral OR gate with complementary outputs

Spice_Node_Sequence: [1] a , [2] b , [3] c , [4] d , [5] e , [6] _Q , [7] Q , [8] com ,

[phidet.asy](#)

Required Refdes Prefix: A

spice_lib: PHASEDET

Description: Behavioral Type 3/4 Phase Detector (phase/frequency detector). NOTE: Limit input rise times

Spice_Node_Sequence: [1] a , [2] b , [3] Q , [4] com ,

[schmitt.asy](#)

Required Refdes Prefix: A

spice_lib: SCHMITT

Description: Behavioral Schmitt-Triggered buffer with complementary outs

Spice_Node_Sequence: [1] in , [2] _Q , [3] Q , [4] com ,

[schmtbuf.asy](#)

Required Refdes Prefix: A

spice_lib: SCHMITT

Description: Behavioral Schmitt-Triggered buffer

Spice_Node_Sequence: [1] in , [2] Q , [3] com ,

[schmtinv.asy](#)

Required Refdes Prefix: A

spice_lib: SCHMITT

Description: Behavioral Schmitt-Triggered inverter

Spice_Node_Sequence: [1] in , [2] _Q , [3] com ,

[srflop.asy](#)

Required Refdes Prefix: A

spice_lib: SRFLOP

Description: Behavioral Set-Reset Flipflop

Spice_Node_Sequence: [1] S , [2] R , [3] _Q , [4] Q , [5] com ,

[xor.asy](#)

Required Refdes Prefix: A

spice_lib: XOR

Description: Behavioral XOR gate

Spice_Node_Sequence: [1] a , [2] b , [3] c , [4] d , [5] e , [6] _Q , [7] Q , [8] com ,

[LT1568.asy](#)

spice_model: LT1568

Required Refdes Prefix: X

spice_lib: LT1568.sub

Description: Very Low Noise, High Frequency Active RC, Filter Building Block

Spice_Node_Sequence: [1] V+ , [2] INVA , [3] SA , [4] OUTA , [5] _OUTA , [6] GNDA , [7] NC , [8] V- , [9] V- , [10] _EN , [11] GNDB , [12] _OUTB , [13] OUTB , [14] SB , [15] INVB , [16] V+ ,

[LT6600-10.asy](#)

spice_model: LT6600-10

Required Refdes Prefix: X

spice_lib: LT6600-10.sub

Description: Very Low Noise, Differential Amplifier and 10MHz Lowpass Filter

Spice_Node_Sequence: [1] IM , [2] VOCM , [3] VCC , [4] OP , [5] OM , [6] VEE , [7] VMID , [8] IP ,

[LT6600-2.5.asy](#)

spice_model: LT6600-2.5

Required Refdes Prefix: X

spice_lib: LT6600-2.5.sub

Description: Very Low Noise, Differential Amplifier and 2.5MHz Lowpass Filter

Spice_Node_Sequence: [1] IM , [2] VOCM , [3] VCC , [4] OP , [5] OM , [6] VEE , [7] VMID , [8] IP ,

[LT6600-20.asy](#)

spice_model: LT6600-20

Required Refdes Prefix: X

spice_lib: LT6600-20.sub

Description: Very Low Noise, Differential Amplifier and 20MHz Lowpass Filter

Spice_Node_Sequence: [1] IM , [2] VOCM , [3] VCC , [4] OP , [5] OM , [6] VEE , [7] VMID , [8] IP ,

[LTC1562-2.asy](#)

spice_model: LTC1562-2

Required Refdes Prefix: X

spice_lib: LTC1562-2.sub

Description: Very Low Noise, Low Distortion Active RC Quad Universal Filter NOTE: GBW, slew rate and noise are modeled in this otherwise highly idealized macromodel.

Spice_Node_Sequence: [1] INV B , [2] V1 B , [3] V2 B , [4] V+ , [5] V2 A , [6] V1 A , [7] INV A , [8] INV D , [9] V1 D , [10] V2 D , [11] AGND , [12] V- , [13] V2 C , [14] V1 C , [15] INV C ,

[LTC1562.asy](#)

spice_model: LTC1562

Required Refdes Prefix: X

spice_lib: LTC1562.sub

Description: Very Low Noise, Low Distortion Active RC Quad Universal Filter NOTE: GBW, slew rate and noise are modeled in this otherwise highly idealized macromodel.

Spice_Node_Sequence: [1] INV B , [2] V1 B , [3] V2 B , [4] V+ , [5] V2 A , [6] V1 A , [7] INV A , [8] INV D , [9] V1 D , [10] V2 D , [11] AGND , [12] V- , [13] V2 C , [14] V1 C , [15] INV C ,

[LTC1562A.asy](#)

spice_model: LTC1562A

Required Refdes Prefix: X

spice_lib: LTC1562.sub

Description: Very Low Noise, Low Distortion Active RC Quad Universal Filter NOTE: GBW, slew rate and noise are modeled in this otherwise highly idealized macromodel.

Spice_Node_Sequence: [1] INV B , [2] V1 B , [3] V2 B , [4] V+ , [5] V2 A , [6] V1 A , [7] INV A , [8] INV D , [9] V1 D , [10] V2 D , [11] AGND , [12] V- , [13] V2 C , [14] V1 C , [15] INV C ,

[LTC1563-2.asy](#)

spice_model: LTC1563-2

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Active RC, 4th Order Lowpass Filter

Spice_Node_Sequence: [1] _LP , [2] SA , [3] INVA , [4] LPA , [5] AGND , [6] V- , [7] _EN , [8] SB , [9] INVB , [10] LPB , [11] V+ ,

[LTC1563-3.asy](#)

spice_model: LTC1563-3

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Active RC, 4th Order Lowpass Filter

Spice_Node_Sequence: [1] _LP , [2] SA , [3] INVA , [4] LPA , [5] AGND , [6] V- , [7] _EN , [8] SB , [9] INVB , [10] LPB , [11] V+ ,

[LTC1565.asy](#)

spice_model: LTC1565

Description: Linear Phase, High Frequency Lowpass Filter

Required Refdes Prefix: X

Spice_Node_Sequence: [1] +IN , [2] -IN , [3] GND , [4] V- , [5] _SHDN , [6] V+ , [7] -OUT , [8] +OUT ,

[2N7002.asy](#)

spice_model: 2N7002

Required Refdes Prefix: X

spice_lib: 2N7002.mod

Description: Small signal N-Channel MOSFET transistor

Spice_Node_Sequence: [1] D , [2] G , [3] S ,

[battery.asy](#)

spice_model: V

Required Refdes Prefix: V

Description: Voltage Source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] + , [2] - ,

[cell.asy](#)

spice_model: V

Required Refdes Prefix: V

Description: Voltage Source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] + , [2] - ,

[DIAC.asy](#)

spice_model: DIAC

Required Refdes Prefix: X

Description: Generic Bi-directional Trigger Device symbol for use with a model that you supply

Spice_Node_Sequence: [1] + , [2] - ,

[DIP10.asy](#)

Required Refdes Prefix: X

Description: Generic Symbol for use with subcircuits that you supply.

Spice_Node_Sequence: [1] 1 , [2] 2 , [3] 3 , [4] 4 , [5] 5 , [6] 6 , [7] 7 , [8] 8 , [9] 9 , [10] 10 ,

[DIP14.asy](#)

Required Refdes Prefix: X

Description: Generic Symbol for use with subcircuits that you supply.

Spice_Node_Sequence: [1] 1 , [2] 2 , [3] 3 , [4] 4 , [5] 5 , [6] 6 , [7] 7 , [8] 8 , [9] 9 , [10] 10 , [11] 11 , [12] 12 , [13] 13 , [14] 14 ,

[DIP16.asy](#)

Required Refdes Prefix: X

Description: Generic Symbol for use with subcircuits that you supply.

Spice_Node_Sequence: [1] 1 , [2] 2 , [3] 3 , [4] 4 , [5] 5 , [6] 6 , [7] 7 , [8] 8 , [9] 9 , [10] 10 , [11] 11 , [12] 12 , [13] 13 , [14] 14 , [15] 15 , [16] 16 ,

[DIP20.asy](#)

Required Refdes Prefix: X

Description: Generic Symbol for use with subcircuits that you supply.

Spice_Node_Sequence: [1] 1 , [2] 2 , [3] 3 , [4] 4 , [5] 5 , [6] 6 , [7] 7 , [8] 8 , [9] 9 , [10] 10 , [11] 11 , [12] 12 , [13] 13 , [14] 14 , [15] 15 , [16] 16 , [17] 17 , [18] 18 , [19] 19 , [20] 20 ,

[DIP8.asy](#)

Required Refdes Prefix: X

Description: Generic Symbol for use with subcircuits that you supply.

Spice_Node_Sequence: [1] 1 , [2] 2 , [3] 3 , [4] 4 , [5] 5 , [6] 6 , [7] 7 , [8] 8 ,

[Epoly.asy](#)

spice_model: POLY()

Required Refdes Prefix: E

Description: Voltage dependent voltage source with two terminals. Useful for drafting schematic generating archaic voltage controlled syntax.

Spice_Node_Sequence: [1] + , [2] - ,

[EuropeanCap.asy](#)

spice_model: C

Required Refdes Prefix: C

Description: Polarized Capacitor

Spice_Node_Sequence: [1] A , [2] B ,

[EuropeanPolcap.asy](#)

spice_model: C

Required Refdes Prefix: C

Description: Polarized Capacitor

Spice_Node_Sequence: [1] A , [2] B ,

[EuropeanResistor.asy](#)

spice_model: R

Required Refdes Prefix: R

Description: A Resistor(European style graphic)

Spice_Node_Sequence: [1] A , [2] B ,

[fixedind.asy](#)

spice_model: L

Required Refdes Prefix: L

Description: An inductor that cannot be edited. Typically used with the 1533/1534

Spice_Node_Sequence: [1] A , [2] B ,

[Gpoly.asy](#)

spice_model: POLY()

Required Refdes Prefix: G

Description: Voltage dependent current source with two terminals. Useful for drafting schematic generating archaic voltage controlled syntax.

Spice_Node_Sequence: [1] + , [2] - ,

[jumper.asy](#)

Required Refdes Prefix: J

Description: A wire jumper. This component lets you give the same net two different names

Spice_Node_Sequence: [1] + , [2] - ,

[NE555.asy](#)

spice_model: NE555

Required Refdes Prefix: X

spice_lib: NE555.sub

Description: An idealized 555 timer model.

Spice_Node_Sequence: [1] GND , [2] TRIG , [3] OUT , [4] RST , [5] CV , [6] THRS , [7] DIS , [8] Vcc ,

[neonbulb.asy](#)

Required Refdes Prefix: X

Description: Parameterized Neon Bulb

spice_lib: neonbulb

Spice_Node_Sequence: [1] V+ , [2] V- ,

[nigbt.asy](#)

spice_model: NIGBT

Required Refdes Prefix: X

Description: N-Channel IGBT symbol for a model you supply.

Spice_Node_Sequence: [1] C , [2] G , [3] E ,
[pentode.asy](#)

spice_model: Pentrode

Required Refdes Prefix: X

Description: This symbol is for use with a subcircuit macromodel that you supply.

Spice_Node_Sequence: [1] Cathode , [2] G1 , [3] G2 , [4] G3 , [5] Anode ,
[pigbt.asy](#)

spice_model: PIGBT

Required Refdes Prefix: X

Description: P-Channel IGBT symbol for a model you supply.

Spice_Node_Sequence: [1] C , [2] G , [3] E ,
[SCR.asy](#)

spice_model: SCR

Required Refdes Prefix: X

Description: Generic SCR symbol for use with a model that you supply.

Spice_Node_Sequence: [1] A , [2] G , [3] K ,
[signal.asy](#)

spice_model: SINE(0 1 1K)

Required Refdes Prefix: V

Description: Voltage Source, either DC, AC, PULSE, SINE, PWL, EXP, or SFFM

Spice_Node_Sequence: [1] + , [2] - ,
[tetrode.asy](#)

spice_model: Tetrode

Required Refdes Prefix: X

Description: This symbol is for use with a subcircuit macromodel that you supply.

Spice_Node_Sequence: [1] Anode , [2] Screen , [3] Grid , [4] Cathode ,
[TowTom2.asy](#)

spice_model: TowTom2

Required Refdes Prefix: X

Description: 2nd Order Tow-Thomas Filter building block. Needs .lib TowTom2.sub & .params R=10K
C=160p GBW1=10Meg GBW2=15Meg Aol=100K

Spice_Node_Sequence: [1] V1 , [2] V2 , [3] INV ,
[TRIAC.asy](#)

spice_model: TRIAC

Required Refdes Prefix: X

Description: Generic TRIAC symbol for use with a model that you supply.

Spice_Node_Sequence: [1] MT2 , [2] G , [3] MT1 ,
[triode.asy](#)

spice_model: Triode

Required Refdes Prefix: X

Description: This symbol is for use with a subcircuit macromodel that you supply.

Spice_Node_Sequence: [1] Plate , [2] Grid , [3] Cathode ,
[urc.asy](#)

spice_model: URC

Required Refdes Prefix: U

Description: Uniform RC-line. Intended for interconnection on IC's but rarely used.

Spice_Node_Sequence: [1] A , [2] B , [3] C ,
[urc2.asy](#)

spice_model: URC

Required Refdes Prefix: U

Description: Uniform RC-line. Intended for interconnection on IC's but rarely used.

Spice_Node_Sequence: [1] A , [2] B , [3] C ,

[xtal.asy](#)

spice_model: C

Required Refdes Prefix: C

Description: Piezoelectric crystal. Set C, Lser and Cpar to set series and parallel resonances. This is actually the same as a capacitor.

Spice_Node_Sequence: [1] A , [2] B ,

[xvaristor.asy](#)

spice_model: varistor

Required Refdes Prefix: X

Description: Generic varistor symbol for use with a model that you supply.

Spice_Node_Sequence: [1] A , [2] B ,

[LT1001.asy](#)

spice_model: LT1001

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1001A.asy](#)

spice_model: LT1001A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1001S8.asy](#)

spice_model: LT1001S8

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1002.asy](#)

spice_model: LT1002

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1002A.asy](#)

spice_model: LT1002A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1006.asy](#)

spice_model: LT1006

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Single Supply Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1006A.asy](#)

spice_model: LT1006A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Single Supply Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1006S8.asy](#)

spice_model: LT1006S8

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Single Supply Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1007.asy](#)

spice_model: LT1007

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low noise, High Speed Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1007A.asy](#)

spice_model: LT1007A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low noise, High Speed Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1007CS.asy](#)

spice_model: LT1007CS

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low noise, High Speed Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1008.asy](#)

spice_model: LT1008

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Picoamp Input Current, μ Volt Offset, Low Noise Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] COMP1 , [7] COMP2 ,
[LT1010.asy](#)

spice_model: LT1010

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Fast +/-150mA Power Buffer

Spice_Node_Sequence: [1] In , [2] V+ , [3] V- , [4] Bias , [5] OUT ,
[LT1012.asy](#)

spice_model: LT1012

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Picoamp Input Current, μ Volt Offset, Low noise Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1012A.asy](#)

spice_model: LT1012A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Picoamp Input Current, μ Volt Offset, Low noise Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1012D.asy](#)

spice_model: LT1012D

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Picoamp Input Current, μ Volt Offset, Low noise Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1012S8.asy](#)

spice_model: LT1012S8

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Picoamp Input Current, μ Volt Offset, Low noise Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1013.asy](#)

spice_model: LT1013

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1013A.asy](#)

spice_model: LT1013A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1013D.asy](#)

spice_model: LT1013D

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1014.asy](#)

spice_model: LT1014

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1014A.asy](#)

spice_model: LT1014A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1014D.asy](#)

spice_model: LT1014D

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1022.asy](#)

spice_model: LT1022

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: High Speed, Precision JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1022A.asy](#)

spice_model: LT1022A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: High Speed, Precision JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1024.asy](#)

spice_model: LT1024

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual, Matched Picoamp μ Volt Input, Low Noise Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1024A.asy](#)

spice_model: LT1024A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual, Matched Picoamp μ Volt Input, Low Noise Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1028.asy](#)

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra-Low Noise Precision High Speed Operational Amplifier

spice_model: LT1028

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1028A.asy](#)

spice_model: LT1028A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra Low Noise Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1028CS.asy](#)

spice_model: LT1028CS

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra Low Noise Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1028N.asy](#)

spice_model: LT1028N

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra-Low Noise Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1037.asy](#)

spice_model: LT1037

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Noise, High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1037A.asy](#)

spice_model: LT1037A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Noise, High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1037CS.asy](#)

spice_model: LT1037CS

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Noise, High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1055.asy](#)

spice_model: LT1055

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Speed, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1055A.asy](#)

spice_model: LT1055A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Speed, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1055S8.asy](#)

spice_model: LT1055S8

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Speed, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1056.asy](#)

spice_model: LT1056

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Speed, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1056A.asy](#)

spice_model: LT1056A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Speed, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1056S8.asy](#)

spice_model: LT1056S8

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Speed, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1057.asy](#)

spice_model: LT1057

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual JFET Input Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1057A.asy](#)

spice_model: LT1057A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual JFET Input Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1057S.asy](#)

spice_model: LT1057S

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual JFET Input Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1058.asy](#)

spice_model: LT1058

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad JFET Input Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1058A.asy](#)

spice_model: LT1058A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad JFET Input Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1077.asy](#)

spice_model: LT1077

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power, Single Supply, Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1077A.asy](#)

spice_model: LT1077A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power, Single Supply, Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1078.asy](#)

spice_model: LT1078

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power, Dual, Single Supply, Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1078A.asy](#)

spice_model: LT1078A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power, Dual, Single Supply, Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1079.asy](#)

spice_model: LT1079

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power, Quad, Single Supply, Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1079A.asy](#)

spice_model: LT1079A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power, Quad, Single Supply, Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1097.asy](#)

spice_model: LT1097

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Cost, Low Power Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1101.asy](#)

spice_model: LT1101

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, μ Power, Single Supply Instrumentation Amplifier(Fixed Gain = 10 or 100)

Spice_Node_Sequence: [1] GND , [2] Tap1 , [3] In- , [4] V- , [5] V+ , [6] In+ , [7] Tap2 , [8] Out ,
[LT1101A.asy](#)

spice_model: LT1101A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, μ Power, Single Supply Instrumentation Amplifier(Fixed Gain = 10 or 100)

Spice_Node_Sequence: [1] GND , [2] Tap1 , [3] In- , [4] V- , [5] V+ , [6] In+ , [7] Tap2 , [8] Out ,
[LT1102.asy](#)

spice_model: LT1102

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: High Speed, Precision, JFET Input Instrumentation Amplifier(Fixed Gain = 10 or 100)

Spice_Node_Sequence: [1] GND , [2] Tap1 , [3] In- , [4] V- , [5] V+ , [6] In+ , [7] Tap2 , [8] Out ,
[LT1102A.asy](#)

spice_model: LT1102A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: High Speed, Precision, JFET Input Instrumentation Amplifier(Fixed Gain = 10 or 100)

Spice_Node_Sequence: [1] GND , [2] Tap1 , [3] In- , [4] V- , [5] V+ , [6] In+ , [7] Tap2 , [8] Out ,
[LT1112.asy](#)

spice_model: LT1112

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Low Power Precision, Picoamp Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1113.asy](#)

spice_model: LT1113

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Low Noise, Precision, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1114.asy](#)

spice_model: LT1114

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Low Power Precision, Picoamp Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1115.asy](#)

spice_model: LT1115

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra-Low Noise, Low Distortion, Audio Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1122.asy](#)

spice_model: LT1122

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Fast Settling, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1124.asy](#)

spice_model: LT1124

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Operational Amplifier Dual Low Noise, High Speed Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1125.asy](#)

spice_model: LT1125

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Operational Amplifier Quad Low Noise, High Speed Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1126.asy](#)

spice_model: LT1126

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Decompensated Low Noise High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1127.asy](#)

spice_model: LT1127

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Decompensated Low Noise High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1128.asy](#)

spice_model: LT1128

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra-Low Noise Precision High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1167.asy](#)

spice_model: LT1167

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Single Resistor Gain Programmable, Precision Instrumentation Amplifier

Spice_Node_Sequence: [1] Rg , [2] In- , [3] In+ , [4] V- , [5] Ref , [6] Out , [7] V+ , [8] Rg ,

[LT1168.asy](#)

spice_model: LT1168

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power, Single Resistor Gain Programmable, Precision Instrumentation Amplifier

Spice_Node_Sequence: [1] RG1 , [2] FB , [3] In+ , [4] V- , [5] REF , [6] OUT , [7] V+ , [8] RG2 ,

[LT1169.asy](#)

spice_model: LT1169

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Low Noise, Picoampere Bias Current, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1178.asy](#)

spice_model: LT1178

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 17mA Max, Dual Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1178A.asy](#)

spice_model: LT1178A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 17mA Max, Dual Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1179.asy](#)

spice_model: LT1179

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 17mA Max, Quad Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1179A.asy](#)

spice_model: LT1179A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 17mA Max, Quad Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1187.asy](#)

spice_model: LT1187

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Power Video Difference Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] REF , [7] FB ,

[LT1189.asy](#)

spice_model: LT1189

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Power Video Difference Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] REF , [7] FB ,

[LT118A.asy](#)

spice_model: LT118A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1190.asy](#)

spice_model: LT1190

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra High Speed Operational Amplifier($A_v \geq 1$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1191.asy](#)

spice_model: LT1191

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1192.asy](#)

spice_model: LT1192

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Ultra High Speed Operational Amplifier($A_v \geq 5$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1193.asy](#)

spice_model: LT1193

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Video Difference Amplifier, Adjustable Gain

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] REF , [7] FB ,

[LT1194.asy](#)

spice_model: LT1194

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Video Difference Amplifier, Gain of 10

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] REF , [7] VOS1 , [8] VOS2 ,

[LT1195.asy](#)

spice_model: LT1195

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Power, Ultra High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[Lt1206.asy](#)

spice_model: LT1206

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 250mA/60MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] COMP ,

[LT1207.asy](#)

spice_model: LT1207

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 250mA/60MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] COMP ,

[LT1208.asy](#)

spice_model: LT1208

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 45MHz, 400V/ μ s Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1209.asy](#)

spice_model: LT1209

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 45MHz, 400V/ μ s Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1210.asy](#)

spice_model: LT1210

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 1.1A, 35MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] CMP ,
[LT1211.asy](#)

spice_model: LT1211

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 14MHz, 7V/ μ s, Single Supply Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1212.asy](#)

spice_model: LT1212

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 14MHz, 7V/ μ s, Single Supply Quad Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1213.asy](#)

spice_model: LT1213

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 28MHz, 12V/ μ s, Single Supply Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1214.asy](#)

spice_model: LT1214

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 28MHz, 12V/ μ s, Single Supply Quad Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1215.asy](#)

spice_model: LT1215

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 23MHz, 50V/ μ s, Single Supply Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1216.asy](#)

spice_model: LT1216

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 23MHz, 50V/ μ s, Single Supply Quad Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1217.asy](#)

spice_model: LT1217

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Power High Speed Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1218.asy](#)

spice_model: LT1218

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Rail-to-Rail Input and Output Op Amp

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,
[LT1219.asy](#)

spice_model: LT1219

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,
[LT1220.asy](#)

spice_model: LT1220

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Very High Speed Operational Amplifier($A_v \geq 1$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1221.asy](#)

spice_model: LT1221

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Very High Speed Operational Amplifier($A_v \geq 4$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1222.asy](#)

spice_model: LT1222

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Noise, Very High Speed Operational Amplifier($A_v \geq 10$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] Comp ,
[LT1223.asy](#)

spice_model: LT1223

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 100MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1224.asy](#)

spice_model: LT1224

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Very High Speed Operational Amplifier($A_v \geq 1$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1225.asy](#)

spice_model: LT1225

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Very High Speed Operational Amplifier($A_v \geq 5$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1226.asy](#)

spice_model: LT1226

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Very High Speed Operational Amplifier($A_v \geq 25$)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1227.asy](#)

spice_model: LT1227

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 140MHz Video Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1228.asy](#)

spice_model: LT1228

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 100MHz Current Feedback Amplifier with DC Gain Control

Spice_Node_Sequence: [1] Iout , [2] In- , [3] In+ , [4] V- , [5] Iset , [6] Vout , [7] V+ , [8] Gain ,
[LT1229.asy](#)

spice_model: LT1229

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 100MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1230.asy](#)

spice_model: LT1230

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 100MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1251.asy](#)

spice_model: LT1251

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 40MHz Video Fader and DC Gain Controlled Amplifier

Spice_Node_Sequence: [1] IN1 , [2] FB1 , [3] Vc , [4] Ic , [5] Rc , [6] NULL , [7] V- , [8] Vout , [9] V+ ,
[10] Rfs , [11] Ifs , [12] Vfs , [13] FB2 , [14] IN2 ,

[LT1252.asy](#)

spice_model: LT1252

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Cost Video Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1253.asy](#)

spice_model: LT1253

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Low Cost Video Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1254.asy](#)

spice_model: LT1254

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Low Cost Video Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1256.asy](#)

spice_model: LT1256

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 40MHz Video Fader and DC Gain Controlled Amplifier

Spice_Node_Sequence: [1] IN1 , [2] FB1 , [3] Vc , [4] Ic , [5] Rc , [6] NULL , [7] V- , [8] Vout , [9] V+ ,
[10] Rfs , [11] Ifs , [12] Vfs , [13] FB2 , [14] IN2 ,

[LT1259.asy](#)

spice_model: LT1259

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Cost Dual 130MHz Current Feedback Amplifier with Shutdown

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1260.asy](#)

spice_model: LT1260

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Cost Triple 130MHz Current Feedback Amplifier with Shutdown

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1328.asy](#)

spice_model: LT1328

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 4Mbps IrDA Infrared Receiver -- simulate with LT1328PD, the IR diode

Spice_Node_Sequence: [1] IN , [2] FIL , [3] SW , [4] GND , [5] DATA , [6] VCC , [7] MODE , [8] Vbias ,

[LT1328PD.asy](#)

spice_model: LT1328PD

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: A photodiode similar to a SFH-205 for testing the LT1328, drive the light input with a current source

Spice_Node_Sequence: [1] A , [2] C , [3] L ,

[LT1351.asy](#)

spice_model: LT1351

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 250 μ A, 3MHz, 200V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1352.asy](#)

spice_model: LT1352

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 250mA, 3MHz, 200V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1353.asy](#)

spice_model: LT1353

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 250mA, 3MHz, 200V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1354.asy](#)

spice_model: LT1354

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 12MHz, 400V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1355.asy](#)

spice_model: LT1355

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 12MHz, 400V/ μ s, Operational Amplifier
Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1356.asy](#)
spice_model: LT1356
Required Refdes Prefix: X
spice_lib: LTC.lib
Description: Quad 12MHz, 400V/ μ s, Operational Amplifier
Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1357.asy](#)
spice_model: LT1357
Required Refdes Prefix: X
spice_lib: LTC.lib
Description: 25MHz, 600V/ μ s, Operational Amplifier
Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1358.asy](#)
spice_model: LT1358
Required Refdes Prefix: X
spice_lib: LTC.lib
Description: Dual 25MHz, 600V/ μ s, Operational Amplifier
Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1359.asy](#)
spice_model: LT1359
Required Refdes Prefix: X
spice_lib: LTC.lib
Description: Quad 25MHz, 600V/ μ s, Operational Amplifier
Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1360.asy](#)
spice_model: LT1360
Required Refdes Prefix: X
spice_lib: LTC.lib
Description: 50MHz, 800V/ μ s, Operational Amplifier
Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1361.asy](#)
spice_model: LT1361
Required Refdes Prefix: X
spice_lib: LTC.lib
Description: Dual 50MHz, 800V/ μ s, Operational Amplifier
Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1362.asy](#)
spice_model: LT1362
Required Refdes Prefix: X
spice_lib: LTC.lib
Description: Quad 50MHz, 800V/ μ s, Operational Amplifier
Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1363.asy](#)
spice_model: LT1363
Required Refdes Prefix: X
spice_lib: LTC.lib
Description: 70MHz, 1000V/ μ s, Operational Amplifier
Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1364.asy](#)
spice_model: LT1364
Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 70MHz, 1000V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1365.asy](#)

spice_model: LT1365

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 70MHz, 1000V/ μ s, Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1366.asy](#)

spice_model: LT1366

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1367.asy](#)

spice_model: LT1367

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1368.asy](#)

spice_model: LT1368

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1369.asy](#)

spice_model: LT1369

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1395.asy](#)

spice_model: LT1395

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 400MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1396.asy](#)

spice_model: LT1396

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 400MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1397.asy](#)

spice_model: LT1397

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 400MHz Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1398.asy](#)

spice_model: LT1398

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Low Cost 300MHz Current Feedback Amplifier with Shutdown

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,
[LT1399.asy](#)

spice_model: LT1399

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Triple Low Cost 300MHz Current Feedback Amplifier with Shutdown

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,
[LT1413.asy](#)

spice_model: LT1413

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Single Supply, Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1413A.asy](#)

spice_model: LT1413A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Single Supply, Dual Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1431.asy](#)

spice_model: LT1431

Required Refdes Prefix: X

spice_lib: LT1431.sub

Description: Programmable Reference

Spice_Node_Sequence: [1] Coll , [2] Comp , [3] V+ , [4] Rtop , [5] GND-S , [6] GND-F , [7] Rmid , [8] Ref
,
[LT1457.asy](#)

spice_model: LT1457

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual Precision JFET Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,
[LT1462.asy](#)

spice_model: LT1462

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual μ Power, 260 μ W C-Load Picoampere Bias Current JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1463.asy](#)

spice_model: LT1463

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad μ Power, 260 μ W C-Load Picoampere Bias Current JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1464.asy](#)

spice_model: LT1464

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual μ Power, 1MHz C-Load Picoampere Bias Current JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1465.asy](#)

spice_model: LT1465

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad μ Power, 1MHz C-Load Picoampere Bias Current JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1466.asy](#)

spice_model: LT1466

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual μ Power, Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1466L.asy](#)

spice_model: LT1466L

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual μ Power, Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1467.asy](#)

spice_model: LT1467

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad μ Power, Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1467L.asy](#)

spice_model: LT1467L

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad μ Power, Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1468-2.asy](#)

spice_model: LT1468-2

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: 90MHz, 22V/ μ s, 16-Bit Accurate Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1468.asy](#)

spice_model: LT1468

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 90MHz, 22V/ μ s, 16-Bit Accurate Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1468N.asy](#)

spice_model: LT1468N

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 90MHz, 22V/ μ s, 16-Bit Accurate Operational Amplifier -- This version of the model includes noise

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1469-2.asy](#)

spice_model: LT1469-2

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: Dual 90MHz, 22V/ μ s, 16-Bit Accurate Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1469.asy](#)

spice_model: LT1469

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 90MHz, 22V/ μ s, 16-Bit Accurate Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1490.asy](#)

spice_model: LT1490

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual μ Power Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1491.asy](#)

spice_model: LT1491

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad μ Power Precision Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1492.asy](#)

spice_model: LT1492

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 5MHz, 3V/ μ s Low Power Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1493.asy](#)

spice_model: LT1493

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 5MHz, 3V/ μ s Low Power Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1494.asy](#)

spice_model: LT1494

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 1.5 μ A Max Over-the-Top Precision Rail-to-Rail Input and Output Opamp

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1495.asy](#)

spice_model: LT1495

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 1.5 μ A Max Over-the-Top Precision Rail-to-Rail Input and Output Opamp(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1496.asy](#)

spice_model: LT1496

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 1.5 μ A Max, Precision Rail-to-Rail Input and Output Operational Amplifier(1 of 4)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1497.asy](#)

spice_model: LT1497

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 125mA, 50MHz, Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1498.asy](#)

spice_model: LT1498

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 10MHz, 6V/ μ s, Rail-to-Rail Input and Output Precision C-Load Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1499.asy](#)

spice_model: LT1499

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 10MHz, 6V/ μ s, Rail-to-Rail Input and Output Precision C-Load Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1630.asy](#)

spice_model: LT1630

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 30MHz, 10V/ μ s, Rail-to-Rail Input and Output Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1631.asy](#)

spice_model: LT1631

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 30MHz, 10V/ μ s, Rail-to-Rail Input and Output Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1632.asy](#)

spice_model: LT1632

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 45MHz, 45V/ μ s, Rail-to-Rail Input and Output Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1633.asy](#)

spice_model: LT1633

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 45MHz, 45V/ μ s, Rail-to-Rail Input and Output Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1635.asy](#)

spice_model: LT1635

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power Rail-to-Rail Operational Amplifier and Reference

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] RefOut , [7] RefFB ,
[LT1636.asy](#)

spice_model: LT1636

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Over-The-Top μ Power Rail-to-Rail Input and Output Operational Amplifier with Shutdown

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,
[LT1637.asy](#)

spice_model: LT1637

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 1.1MHz, 0.4V/ μ s Over-The-Top μ Power Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1638.asy](#)

spice_model: LT1638

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 1.2MHz, 0.4V/ μ s, Over-The-Top μ Power Rail-to-Rail input and output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1639.asy](#)

spice_model: LT1639

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 1.2MHz, 0.4V/ μ s, Over-The-Top μ Power Rail-to-Rail input and output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1672.asy](#)

spice_model: LT1672

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Single 2 μ A, $A_v > 5$ Over-The-Top Precision Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1673.asy](#)

spice_model: LT1673

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 2 μ A, $A_v > 5$ Over-The-Top Precision Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1674.asy](#)

spice_model: LT1674

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad 2 μ A, $A_v > 5$ Over-The-Top Precision Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1677.asy](#)

spice_model: LT1677

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Noise, Rail-to-Rail Input and Output Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1678.asy](#)

spice_model: LT1678

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Low Noise, Rail-to-Rail, Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT1679.asy](#)

spice_model: LT1679

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Low Noise, Rail-to-Rail, Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,
[LT1722.asy](#)

spice_model: LT1722

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 200MHz Low Noise Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1723.asy](#)

spice_model: LT1723

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 200MHz Low Noise Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1724.asy](#)

spice_model: LT1724

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 200MHz Low Noise Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,
[LT1782.asy](#)

spice_model: LT1782

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: μ Power Over-The-Top, SOT-23, Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,
[LT1783.asy](#)

spice_model: LT1783

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 1.2MHz μ Power Over-The-Top, SOT-23, Rail-to-Rail Input and Output Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,
[LT1784.asy](#)

spice_model: LT1784

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 2.5MHz Over-The-Top Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE , [6] SHDN ,
[LT1787.asy](#)

spice_model: LT1787

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Precision, High Side Current Sense Amplifier

Spice_Node_Sequence: [1] FIL- , [2] Vs- , [3] VEE , [4] Vout , [5] Vbias , [6] Vs+ , [7] FIL+ ,
[LT1789-1.asy](#)

spice_model: LT1789-1

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: μ Power, Single Supply Rail-to-Rail Output Instrumentation Amplifier

Spice_Node_Sequence: [1] RG1 , [2] IN- , [3] IN+ , [4] VEE , [5] REF , [6] OUT , [7] VCC , [8] RG2 ,
[LT1789-10.asy](#)

spice_model: LT1789-10

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: μ Power, Single Supply Rail-to-Rail Output Instrumentation Amplifier

Spice_Node_Sequence: [1] RG1 , [2] IN- , [3] IN+ , [4] VEE , [5] REF , [6] OUT , [7] VCC , [8] RG2 , [LT1792.asy](#)

spice_model: LT1792

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Noise, Precision, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [LT1793.asy](#)

spice_model: LT1793

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Low Noise, Picoampere Bias Current, JFET Input Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [LT1795.asy](#)

spice_model: LT1795

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 500mA/50MHz Current Feedback Amplifier/xDSL Line Driver

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDNREF , [7] SHDN , [8] COMP , [LT1797.asy](#)

spice_model: LT1797

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 10MHz, Rail-to-Rail Input and Output Op Amp in SOT-23

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [LT1800.asy](#)

spice_model: LT1800

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 80MHz, 25V/ μ s Low Power Rail-to-Rail Input and Output Precision Op Amp

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [LT1801.asy](#)

spice_model: LT1801

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 80MHz, 25V/ μ s Low Power Rail-to-Rail Input and Output Precision Op Amp(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [LT1802.asy](#)

spice_model: LT1802

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 80MHz, 25V/ μ s Low Power Rail-to-Rail Input and Output Precision Op Amp(1 of 4)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [LT1803.asy](#)

spice_model: LT1803

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 100V/ μ s, 85MHz Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT , [LT1804.asy](#)

spice_model: LT1804

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 100V/ μ s, 85MHz Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1805.asy](#)

spice_model: LT1805

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad 100V/ μ s, 85MHz Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1806.asy](#)

spice_model: LT1806

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 325MHz, Single Rail-to-Rail Input and Output, Low Distortion, Low Noise Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1807.asy](#)

spice_model: LT1807

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 325MHz, Dual Rail-to-Rail Input and Output, Low Distortion, Low Noise Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1809.asy](#)

spice_model: LT1809

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Single 180MHz, 350V/ μ s, Rail-to-Rail Input and Output, Low Distortion, Low Noise Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] SHDN ,

[LT1810.asy](#)

spice_model: LT1810

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 180MHz, 350V/ μ s, Rail-to-Rail Input and Output, Low Distortion, Low Noise Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1812.asy](#)

spice_model: LT1812

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: 3mA, 100MHz, 750V/ μ s Operational Amplifier with Shutdown

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1813.asy](#)

spice_model: LT1813

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 3mA, 100MHz, 750V/ μ s Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1814.asy](#)

spice_model: LT1814

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 3mA, 100MHz, 750V/ μ s Operational Amplifier(1 of 4)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1815.asy](#)

spice_model: LT1815

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 220MHz, 1500V/ μ s Operational Amplifier

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT1816.asy](#)

spice_model: LT1816

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 220MHz, 1500V/ μ s Operational Amplifier

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1817.asy](#)

spice_model: LT1817

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad 220MHz, 1500V/ μ s Operational Amplifier

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1818.asy](#)

spice_model: LT1818

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 400MHz, 2500V/ μ s, 9mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT1819.asy](#)

spice_model: LT1819

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 400MHz, 2500V/ μ s, 9mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT1880.asy](#)

spice_model: LT1880

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Rail-to-Rail Output pA Input Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1881.asy](#)

spice_model: LT1881

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Rail-to-Rail Output pA Input Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1882.asy](#)

spice_model: LT1882

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Rail-to-Rail Output pA Input Precision Operational Amplifier(1 of 4)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1884.asy](#)

spice_model: LT1884

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual Rail-to-Rail Output pA Input Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1885.asy](#)

spice_model: LT1885

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad Rail-to-Rail Output pA Input Precision Operational Amplifier(1 of 4)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1886.asy](#)

spice_model: LT1886

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual 700MHz, $A_v \geq 10$, 200mA Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT1920.asy](#)

spice_model: LT1920

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Single Resistor Gain Programmable, Precision Instrumentation Amplifier

Spice_Node_Sequence: [1] Rg , [2] In- , [3] In+ , [4] V- , [5] Ref , [6] Out , [7] V+ , [8] Rg ,

[LT1970.asy](#)

spice_model: LT1970

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: 500mA Power Op Amp with Adjustable Precision Current Limit

Spice_Node_Sequence: [1] Vee , [2] V- , [3] OUT , [4] Sense+ , [5] Filter , [6] Sense- , [7] Vcc , [8] -IN , [9] +IN , [10] VCsnk , [11] VCsrc , [12] COM , [13] Enable , [14] _Isrc , [15] _Isnk , [16] _TSD , [17] V+ ,

[LT1990.asy](#)

spice_model: LT1990

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: $\pm 250V$ Input Range $G=1, 10 \mu$ Power Difference Amplifier

Spice_Node_Sequence: [1] REF , [2] IN- , [3] IN+ , [4] VEE , [5] GAIN2 , [6] OUT , [7] VCC , [8] GAIN1 ,

[LT1991.asy](#)

spice_model: LT1991

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Precision, 100 μA Gain Selectable Amplifier

Spice_Node_Sequence: [1] P1 , [2] P3 , [3] P9 , [4] Vee , [5] Ref , [6] OUT , [7] VCC , [8] M9 , [9] M3 , [10] M1 ,

[LT1993-10.asy](#)

spice_model: LT1993-10

Required Refdes Prefix: X

spice_lib: LTC3.LIB

Description: 700MHz Low Distortion, Low Noise Differential Amplifier and ADC Driver

Spice_Node_Sequence: [1] VccC , [2] VOCM , [3] VccA , [4] VeeA , [5] +OUT , [6] +OUTFILT , [7] -OUTFILT , [8] -OUT , [9] VeeB , [10] VccB , [11] _EN , [12] VeeC , [13] -INB , [14] -INA , [15] +INB , [16] +INA ,

[LT1993-2.asy](#)

spice_model: LT1993-2

Required Refdes Prefix: X

spice_lib: LT1993-2.LIB

Description: 800MHz Low Distortion, Low Noise Differential Amplifier and ADC Driver

Spice_Node_Sequence: [1] VccC , [2] VOCM , [3] VccA , [4] VeeA , [5] +OUT , [6] +OUTFILT , [7] -OUTFILT , [8] -OUT , [9] VeeB , [10] VccB , [11] _EN , [12] VeeC , [13] -INB , [14] -INA , [15] +INB , [16] +INA ,

[LT1993-4.asy](#)

spice_model: LT1993-4

Required Refdes Prefix: X

spice_lib: LTC3.LIB

Description: 900MHz Low Distortion, Low Noise Differential Amplifier and ADC Driver

Spice_Node_Sequence: [1] VccC , [2] VOCM , [3] VccA , [4] VeeA , [5] +OUT , [6] +OUTFILT , [7] -OUTFILT , [8] -OUT , [9] VeeB , [10] VccB , [11] _EN , [12] VeeC , [13] -INB , [14] -INA , [15] +INB , [16] +INA ,

[LT1994.asy](#)

spice_model: LT1994

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: Low Noise, Low Distortion Fully Differential I/O Amplifier/Driver

Spice_Node_Sequence: [1] IN- , [2] VOCM , [3] V+ , [4] OUT+ , [5] OUT- , [6] V- , [7] SHDN , [8] IN+ ,

[LT1995.asy](#)

spice_model: LT1995

Required Refdes Prefix: X

spice_lib: LTC7.lib

Description: 30MHz,1000V/use Gain Selectable Amplifier

Spice_Node_Sequence: [1] P1 , [2] P2 , [3] P4 , [4] Vee , [5] Ref , [6] OUT , [7] VCC , [8] M4 , [9] M2 , [10] M1 ,

[LT1996.asy](#)

spice_model: LT1996

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Precision, 100 μ A Gain Selectable Amplifier

Spice_Node_Sequence: [1] P9 , [2] P27 , [3] P81 , [4] Vee , [5] Ref , [6] OUT , [7] VCC , [8] M81 , [9] M27 , [10] M9 ,

[LT1999-10.asy](#)

spice_model: LT1999-10

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: High Voltage, Bidirectional Current Sense Amplifier

Spice_Node_Sequence: [1] V+ , [2] +IN , [3] -IN , [4] GND , [5] Ref , [6] Vout , [7] _SHDN ,

[LT1999-20.asy](#)

spice_model: LT1999-20

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: High Voltage, Bidirectional Current Sense Amplifier

Spice_Node_Sequence: [1] V+ , [2] +IN , [3] -IN , [4] GND , [5] Ref , [6] Vout , [7] _SHDN ,

[LT1999-50.asy](#)

spice_model: LT1999-50

Required Refdes Prefix: X

spice_lib: LTC6.lib

Description: High Voltage, Bidirectional Current Sense Amplifier

Spice_Node_Sequence: [1] V+ , [2] +IN , [3] -IN , [4] GND , [5] Ref , [6] Vout , [7] _SHDN ,

[LT2078.asy](#)

spice_model: LT2078

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual μ Power, Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT2078A.asy](#)

spice_model: LT2078A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Dual μ Power, Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT2079.asy](#)

spice_model: LT2079

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad μ Power, Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT2079A.asy](#)

spice_model: LT2079A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad μ Power, Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT2178.asy](#)

spice_model: LT2178

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 17mA Max, Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT2178A.asy](#)

spice_model: LT2178A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 17mA Max, Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT2179.asy](#)

spice_model: LT2179

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 17mA Max, Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT2179A.asy](#)

spice_model: LT2179A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Quad 17mA Max, Single Supply Precision Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT318A.asy](#)

spice_model: LT318A

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: High Speed Operational Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT ,

[LT6000.asy](#)

spice_model: LT6000

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: 1.8V, 13 μ A Precision Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT , [6] SHDN_L ,

[LT6001.asy](#)

spice_model: LT6001

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: Dual 1.8V, 13 μ A Precision Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT , [6] SHDN_L ,

[LT6002.asy](#)

spice_model: LT6002

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: Quad 1.8V, 13 μ A Precision Rail-to-Rail Op Amps

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT6003.asy](#)

spice_model: LT6003

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: 1.6V, 1 μ A Precision Rail-to-Rail Input and Output Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT6004.asy](#)

spice_model: LT6004

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: Dual 1.6V, 1 μ A Precision Rail-to-Rail Input and Output Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT6005.asy](#)

spice_model: LT6005

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: Quad 1.6V, 1 μ A Precision Rail-to-Rail Input and Output Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT6010.asy](#)

spice_model: LT6010

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 135 μ A, 14nV/rtHz, Rail-to-Rail Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT , [6] SHDN ,

[LT6011.asy](#)

spice_model: LT6011

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 135 μ A, 14nV/rtHz, Rail-to-Rail Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT6012.asy](#)

spice_model: LT6012

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad 135 μ A, 14nV/rtHz, Rail-to-Rail Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,
[LT6013.asy](#)

spice_model: LT6013

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 145 μ A, 9.5nV/rtHz, $A_v > 5$, Rail-to-Rail Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,
[LT6014.asy](#)

spice_model: LT6014

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 145 μ A, 9.5nV/rtHz, $A_v > 5$, Rail-to-Rail Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,
[LT6100.asy](#)

spice_model: LT6100

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: Precision, Gain Selectable High Side Current Sense Amplifier

Spice_Node_Sequence: [1] VS- , [2] VCC , [3] FIL , [4] VEE , [5] VOUT , [6] A2 , [7] A4 , [8] VS+ ,
[LT6106.asy](#)

spice_model: LT6106

Required Refdes Prefix: X

spice_lib: LTC7.lib

Description: High Voltage, High-Side Current Sense in SOT-23

Spice_Node_Sequence: [1] OUT , [2] VEE , [3] IN- , [4] IN+ , [5] VCC ,
[LT6200-10.asy](#)

spice_model: LT6200-10

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 1600MHz, $A_v=10$ Rail-to-Rail Low Noise Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE , [6] SHDN_L ,
[LT6200-5.asy](#)

spice_model: LT6200-5

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 800MHz, $A_v=5$ Rail-to-Rail Low Noise Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE , [6] SHDN_L ,
[LT6200.asy](#)

spice_model: LT6200

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 165 MHz, Rail-to-Rail Input and Output, 0.95nV/rtHz Low Noise Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE , [6] SHDN_L ,
[LT6201.asy](#)

spice_model: LT6201

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 165 MHz, Rail-to-Rail, 0.95 nV/rtHz Low Noise Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,
[LT6202.asy](#)

spice_model: LT6202

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 100 MHz, Rail-to-Rail, 1.9nV/rtHz Low Power Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT6203.asy](#)

spice_model: LT6203

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 100 MHz, Rail-to-Rail, 1.9nV/rtHz Low Power Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT6204.asy](#)

spice_model: LT6204

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad 100 MHz, Rail-to-Rail, 1.9nV/rtHz Low Power Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT6205.asy](#)

spice_model: LT6205

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Single Supply 3V, 100MHz Video Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT6206.asy](#)

spice_model: LT6206

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual Single Supply 3V, 100MHz Video Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT6207.asy](#)

spice_model: LT6207

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad Single Supply 3V, 100MHz Video Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT6210.asy](#)

spice_model: LT6210

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: Programmable Supply Current, R-R Output, Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] V-1 ,

[LT6211.asy](#)

spice_model: LT6211

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: Dual Programmable Supply Current, R-R Output, Current Feedback Amplifier

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] V-1 ,

[LT6220.asy](#)

spice_model: LT6220

Required Refdes Prefix: X

spice_lib: LTC4.LIB

Description: 60MHz, 20V/us, Low Power, RRIO Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,

[LT6221.asy](#)

spice_model: LT6221

Required Refdes Prefix: X

spice_lib: LTC4.LIB

Description: Dual 60MHz, 20V/us, Low Power, RRIO Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,
[LT6222.asy](#)

spice_model: LT6222

Required Refdes Prefix: X

spice_lib: LTC4.LIB

Description: Quad 60MHz, 20V/us, Low Power, RRIO Precision Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,
[LT6230-10.asy](#)

spice_model: LT6230-10

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 1.45GHz, Av=10, Rail-to-Rail Output, Low Noise, 3.4mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE , [6] EN_L ,
[LT6230.asy](#)

spice_model: LT6230

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 215 MHz, Rail-to-Rail Output, 1.1nV/rtHz, 3.4mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT , [6] SHDN ,
[LT6231.asy](#)

spice_model: LT6231

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 215 MHz, Rail-to-Rail Output, 1.1nV/rtHz, 3.4mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,
[LT6232.asy](#)

spice_model: LT6232

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad 215 MHz, Rail-to-Rail Output, 1.1nV/rtHz, 3.4mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,
[LT6233-10.asy](#)

spice_model: LT6233-10

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 375MHz, AV=10, Rail-to-Rail Output, 1.9nV/rtHz, 1.2mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE , [6] EN_L ,
[LT6233.asy](#)

spice_model: LT6233

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 60MHz, Rail-to-Rail Output, 1.9nV/rtHz, 1.2mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE , [6] EN_L ,
[LT6234.asy](#)

spice_model: LT6234

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Dual 60MHz, Rail-to-Rail Output, 1.9nV/rtHz, 1.2mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] VCC , [4] VEE , [5] OUT ,
[LT6235.asy](#)

spice_model: LT6235

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: Quad 60MHz, Rail-to-Rail Output, 1.9nV/rtHz, 1.2mA Op Amp

Spice_Node_Sequence: [1] IN+ , [2] IN- , [3] OUT , [4] VCC , [5] VEE ,

[LT6350.asy](#)

spice_model: LT6350

Required Refdes Prefix: X

Description: Single-Ended to Differential Converter/ADC Driver

spice_lib: LTC7.lib

Spice_Node_Sequence: [1] -IN1 , [2] +IN2 , [3] V+ , [4] OUT1 , [5] OUT2 , [6] V- , [7] _SHDN , [8] +IN1 ,

[LT6402-12.asy](#)

spice_model: LT6402-12

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: 300MHz Low Distortion, Low Noise Differential Amplifier/ADC Driver

Spice_Node_Sequence: [1] VccC , [2] VOCM , [3] VccA , [4] VeeA , [5] +OUT , [6] +OUTFILT , [7] -OUTFILT , [8] -OUT , [9] VeeB , [10] VccB , [11] _EN , [12] VeeC , [13] -INB , [14] -INA , [15] +INB , [16] +INA ,

[LT6402-20.asy](#)

spice_model: LT6402-20

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: 300MHz Low Distortion, Low Noise Differential Amplifier/ADC Driver

Spice_Node_Sequence: [1] VccC , [2] VOCM , [3] VccA , [4] VeeA , [5] +OUT , [6] +OUTFILT , [7] -OUTFILT , [8] -OUT , [9] VeeB , [10] VccB , [11] _EN , [12] VeeC , [13] -INB , [14] -INA , [15] +INB , [16] +INA ,

[LT6402-6.asy](#)

spice_model: LT6402-6

Required Refdes Prefix: X

spice_lib: LTC5.lib

Description: 300MHz Low Distortion, Low Noise Differential Amplifier/ADC Driver

Spice_Node_Sequence: [1] VccC , [2] VOCM , [3] VccA , [4] VeeA , [5] +OUT , [6] +OUTFILT , [7] -OUTFILT , [8] -OUT , [9] VeeB , [10] VccB , [11] _EN , [12] VeeC , [13] -INB , [14] -INA , [15] +INB , [16] +INA ,

[LT6411.asy](#)

spice_model: LT6411

Required Refdes Prefix: X

spice_lib: LTC4.lib

Description: 650MHz Differential ADC Driver/Dual Selectable Gain Amplifier

Spice_Node_Sequence: [1] Vee , [2] OUT2 , [3] Vcc , [4] OUT1 , [5] _EN , [6] DGND , [7] IN1+ , [8] IN1- , [9] IN2- , [10] IN2+ ,

[LT6550.asy](#)

spice_model: LT6550

Required Refdes Prefix: X

spice_lib: LTC2.LIB

Description: 3.3V Triple Video Amplifier

Spice_Node_Sequence: [1] IN , [2] GND , [3] VCC , [4] VEE , [5] OUT ,

[LT6551.asy](#)

spice_model

[4N25.asy](#)

spice_model: 4N25

Required Refdes Prefix: X

spice_lib: 4N25.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,

[4N25A.asy](#)

spice_model: 4N25A

Required Refdes Prefix: X

spice_lib: 4N25.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,

[4N26.asy](#)

spice_model: 4N26

Required Refdes Prefix: X

spice_lib: 4N25.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,

[4N27.asy](#)

spice_model: 4N27

Required Refdes Prefix: X

spice_lib: 4N27.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,

[4N28.asy](#)

spice_model: 4N28

Required Refdes Prefix: X

spice_lib: 4N27.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,

[CNY17-1.asy](#)

spice_model: CNY17-1

Required Refdes Prefix: X

spice_lib: CNY17.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,

[CNY17-2.asy](#)

spice_model: CNY17-2

Required Refdes Prefix: X

spice_lib: CNY17.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,

[CNY17-3.asy](#)

spice_model: CNY17-3

Required Refdes Prefix: X

spice_lib: CNY17.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,

[MOC205.asy](#)

spice_model: MOC205

Required Refdes Prefix: X

spice_lib: MOC205.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,

[MOC206.asy](#)

spice_model: MOC206

Required Refdes Prefix: X

spice_lib: MOC205.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,

[MOC207.asy](#)

spice_model: MOC207

Required Refdes Prefix: X

spice_lib: MOC205.sub

Description: Optoisolator, Transistor Output

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E , [5] B ,

[PC817A.asy](#)

spice_model: PC817A

Required Refdes Prefix: X

spice_lib: PC817.sub

Description: High Density Mounting Type Photocoupler

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E ,

[PC817B.asy](#)

spice_model: PC817B

Required Refdes Prefix: X

spice_lib: PC817.sub

Description: High Density Mounting Type Photocoupler

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E ,

[PC817C.asy](#)

spice_model: PC817C

Required Refdes Prefix: X

spice_lib: PC817.sub

Description: High Density Mounting Type Photocoupler

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E ,

[PC817D.asy](#)

spice_model: PC817D

Required Refdes Prefix: X

spice_lib: PC817.sub

Description: High Density Mounting Type Photocoupler

Spice_Node_Sequence: [1] A , [2] K , [3] C , [4] E ,

[LT1026.asy](#)

spice_model: LT1026

Required Refdes Prefix: X

spice_lib: LT1026.sub

Description: Switched Capacitor Voltage Converter

Spice_Node_Sequence: [1] C1- , [2] C2+ , [3] C2- , [4] -Vout , [5] GND , [6] Vin , [7] C1+ , [8] +Vout ,

[LT1054.asy](#)

spice_model: LT1054

Required Refdes Prefix: X

spice_lib: LT1054.sub

Description: Switched-Capacitor Voltage Converter with Regulator

Spice_Node_Sequence: [1] FB , [2] Cap+ , [3] GND , [4] Cap- , [5] Vout , [6] Ref , [7] OSC , [8] Vin ,

[LT1054L.asy](#)

spice_model: LT1054L
Required Refdes Prefix: X
spice_lib: LT1054L.sub
Description: Switched-Capacitor Voltage Converter with Regulator
Spice_Node_Sequence: [1] FB , [2] Cap+ , [3] GND , [4] Cap- , [5] Vout , [6] Ref , [7] OSC , [8] Vin ,
[LT1070.asy](#)

spice_model: LT1070
Required Refdes Prefix: X
spice_lib: LT1070.sub
Description: 5A High Efficiency Switching Regulator
Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,
[LT1070HV.asy](#)

spice_model: LT1070HV
Required Refdes Prefix: X
spice_lib: LT1070.sub
Description: 5A High Efficiency Switching Regulator(High Voltage version)
Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,
[LT1071.asy](#)

spice_model: LT1071
Required Refdes Prefix: X
spice_lib: LT1071.sub
Description: 2.5A High Efficiency Switching Regulator
Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,
[LT1071HV.asy](#)

spice_model: LT1071HV
Required Refdes Prefix: X
spice_lib: LT1071.sub
Description: 2.5A High Efficiency Switching Regulator(High Voltage version)
Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,
[LT1072.asy](#)

spice_model: LT1072
Required Refdes Prefix: X
spice_lib: LT1072.sub
Description: 1.25A High Efficiency Switching Regulator
Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,
[LT1072HV.asy](#)

spice_model: LT1072HV
Required Refdes Prefix: X
spice_lib: LT1072.sub
Description: 1.25A High Efficiency Switching Regulator(High Voltage version)
Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,
[LT1073-12.asy](#)

spice_model: LT1073-12
Required Refdes Prefix: X
spice_lib: LT1073.sub
Description: 12 Volt μ Power DC/DC Converter
Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] FB ,
[LT1073-5.asy](#)

spice_model: LT1073-5
Required Refdes Prefix: X
spice_lib: LT1073.sub
Description: 5 Volt μ Power DC/DC Converter
Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] FB ,

[LT1073.asy](#)

spice_model: LT1073

Required Refdes Prefix: X

spice_lib: LT1073.sub

Description: 5 Volt μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] FB ,

[LT1074.asy](#)

spice_model: LT1074

Required Refdes Prefix: X

spice_lib: LT1074.sub

Description: 5A Step-Down Switching Regulator

Spice_Node_Sequence: [1] Vin , [2] SW , [3] Ilim , [4] GND , [5] FB , [6] Vc , [7] _SHDN ,

[LT1074HV.asy](#)

spice_model: LT1074HV

Required Refdes Prefix: X

spice_lib: LT1074.sub

Description: 5A Step-Down Switching Regulator

Spice_Node_Sequence: [1] Vin , [2] SW , [3] Ilim , [4] GND , [5] FB , [6] Vc , [7] _SHDN ,

[LT1076-5.asy](#)

spice_model: LT1076-5

Required Refdes Prefix: X

spice_lib: LT1076-5.sub

Description: 2A Step-Down Switching Regulator

Spice_Node_Sequence: [1] Vin , [2] SW , [3] Ilim , [4] GND , [5] Sense , [6] Vc , [7] SHDN ,

[LT1076.asy](#)

spice_model: LT1076

Required Refdes Prefix: X

spice_lib: LT1076.sub

Description: 2A Step-Down Switching Regulator

Spice_Node_Sequence: [1] Vin , [2] SW , [3] Ilim , [4] GND , [5] FB , [6] VC , [7] SHDN ,

[LT1076HV.asy](#)

spice_model: LT1076HV

Required Refdes Prefix: X

spice_lib: LT1076.sub

Description: 2A Step-Down Switching Regulator

Spice_Node_Sequence: [1] Vin , [2] SW , [3] Ilim , [4] GND , [5] FB , [6] VC , [7] SHDN ,

[LT1082.asy](#)

spice_model: LT1082

Required Refdes Prefix: X

spice_lib: LT1082.sub

Description: 1A High Voltage, Efficiency Switching Voltage Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,

[LT1083.asy](#)

spice_model: LT1083

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 7.5A Low Dropout Positive Adjustable Regulator

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LT1084.asy](#)

spice_model: LT1084

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 5A Low Dropout Positive Adjustable Regulator

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LT1085.asy](#)

spice_model: LT1085

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 3A Low Dropout Positive Adjustable Regulator

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LT1086-12.asy](#)

spice_model: LT1086-12

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 1.5A Low Dropout Positive Regulator, Fixed 12V Output

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LT1086-2.85.asy](#)

spice_model: LT1086-2.85

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 1.5A Low Dropout Positive Regulator, Fixed 2.85V Output

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LT1086-3.3.asy](#)

spice_model: LT1086-3.3

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 1.5A Low Dropout Positive Regulator, Fixed 3.3V Output

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LT1086-3.6.asy](#)

spice_model: LT1086-3.6

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 1.5A Low Dropout Positive Regulator, Fixed 3.6V Output

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LT1086-5.asy](#)

spice_model: LT1086-5

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 1.5A Low Dropout Positive Regulator, Fixed 5V Output

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LT1086.asy](#)

spice_model: LT1086

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 1.5A Low Dropout Positive Adjustable Regulator

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LT1086H.asy](#)

spice_model: LT1086H

Required Refdes Prefix: X

spice_lib: LT1083.lib

Description: 0.7A Low Dropout Positive Adjustable Regulator

Spice_Node_Sequence: [1] IN , [2] ADJ , [3] OUT ,

[LT1103.asy](#)

spice_model: LT1103

Required Refdes Prefix: X

spice_lib: LT1103.sub

Description: Offline Switching Regulator

Spice_Node_Sequence: [1] SW , [2] FB , [3] Vc , [4] GND , [5] OSC , [6] Vin , [7] 15V ,

[LT1105.asy](#)

spice_model: LT1105

Required Refdes Prefix: X

spice_lib: LT1105.sub

Description: Offline Switching Regulator

Spice_Node_Sequence: [1] GND , [2] Ilim , [3] FB , [4] Vc , [5] OSC , [6] Vin , [7] 15V , [8] SW ,

[LT1106.asy](#)

spice_model: LT1106

Required Refdes Prefix: X

spice_lib: LT1106.sub

Description: μ Power Step-Up DC/DC Converter for PCMCIA Card Flash Memory

Spice_Node_Sequence: [1] SEL 12/5 , [2] Soft Start , [3] Vcc , [4] 3/5 Detect , [5] PGND , [6] SW , [7] SGND , [8] _Vpp Valid , [9] G2 , [10] G1 , [11] Vpp2 , [12] Vpp1 , [13] AVpp , [14] ON/OFF ,

[LT1107-12.asy](#)

spice_model: LT1107-12

Required Refdes Prefix: X

spice_lib: LT1107.sub

Description: 12 Volt μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] AO , [7] SET , [8] Sense ,

[LT1107-5.asy](#)

spice_model: LT1107-5

Required Refdes Prefix: X

spice_lib: LT1107.sub

Description: 5 Volt μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] AO , [7] SET , [8] Sense ,

[LT1107.asy](#)

spice_model: LT1107

Required Refdes Prefix: X

spice_lib: LT1107.sub

Description: Adjustable μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] AO , [7] SET , [8] FB ,

[LT1108-12.asy](#)

spice_model: LT1108-12

Required Refdes Prefix: X

spice_lib: LT1108.sub

Description: 12Volt μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] AO , [7] SET , [8] Sense ,

[LT1108-5.asy](#)

spice_model: LT1108-5

Required Refdes Prefix: X

spice_lib: LT1108.sub

Description: 5Volt μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] AO , [7] SET , [8] Sense ,

[LT1108.asy](#)

spice_model: LT1108

Required Refdes Prefix: X

spice_lib: LT1108.sub

Description: Adjustable μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] AO , [7] SET , [8] FB ,

[LT1109-12.asy](#)

spice_model: LT1109-12

Required Refdes Prefix: X

spice_lib: LT1109.sub

Description: μ Power Low Cost DC/DC Converter, Fixed 12V

Spice_Node_Sequence: [1] Vin , [2] SW , [3] GND , [4] _SDN , [5] Sense ,
[LT1109-5.asy](#)

spice_model: LT1109-5

Required Refdes Prefix: X

spice_lib: LT1109.sub

Description: μ Power Low Cost DC/DC Converter, Fixed 5V

Spice_Node_Sequence: [1] Vin , [2] SW , [3] GND , [4] _SDN , [5] Sense ,
[LT1109.asy](#)

spice_model: LT1109

Required Refdes Prefix: X

spice_lib: LT1109.sub

Description: μ Power Low Cost DC/DC Converter, Adjustable Output Voltage

Spice_Node_Sequence: [1] Vin , [2] SW , [3] GND , [4] _SDN , [5] FB ,
[LT1109A-12.asy](#)

spice_model: LT1109A-12

Required Refdes Prefix: X

spice_lib: LT1109A.sub

Description: μ Power DC/DC Converter Flash Memory VPP Generator, Fixed 12V

Spice_Node_Sequence: [1] Vin , [2] SW , [3] GND , [4] _SDN , [5] Sense ,
[LT1109A-5.asy](#)

spice_model: LT1109A-5

Required Refdes Prefix: X

spice_lib: LT1109A.sub

Description: μ Power DC/DC Converter Flash Memory VPP Generator, Fixed 5V

Spice_Node_Sequence: [1] Vin , [2] SW , [3] GND , [4] _SDN , [5] Sense ,
[LT1109A.asy](#)

spice_model: LT1109A

Required Refdes Prefix: X

spice_lib: LT1109A.sub

Description: μ Power DC/DC Converter Flash Memory VPP Generator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Vin , [2] SW , [3] GND , [4] _SDN , [5] FB ,
[LT1110-12.asy](#)

spice_model: LT1110-12

Required Refdes Prefix: X

spice_lib: LT1110.sub

Description: 12 Volt μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] FB ,
[LT1110-5.asy](#)

spice_model: LT1110-5

Required Refdes Prefix: X

spice_lib: LT1110.sub

Description: 5 Volt μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] FB ,
[LT1110.asy](#)

spice_model: LT1110

Required Refdes Prefix: X

spice_lib: LT1110.sub

Description: Adjustable μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] FB ,
[LT1111-12.asy](#)

spice_model: LT1111-12
Required Refdes Prefix: X
spice_lib: LT1111.sub
Description: 12 Volt μ Power DC/DC Converter
Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] AO , [7] SET , [8] Sense ,
[LT1111-5.asy](#)
spice_model: LT1111-5
Required Refdes Prefix: X
spice_lib: LT1111.sub
Description: 5 Volt μ Power DC/DC Converter
Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] AO , [7] SET , [8] Sense ,
[LT1111.asy](#)
spice_model: LT1111
Required Refdes Prefix: X
spice_lib: LT1111.sub
Description: Adjustable μ Power DC/DC Converter
Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] AO , [7] SET , [8] FB ,
[LT1121-3.3.asy](#)
spice_model: LT1121-3.3
Required Refdes Prefix: X
spice_lib: LT1121.lib
Description: μ Power Low Dropout Regulator with Shutdown, Fixed 3.3V Output
Spice_Node_Sequence: [1] OUT , [2] GND , [3] _SHDN , [4] IN ,
[LT1121-5.asy](#)
spice_model: LT1121-5
Required Refdes Prefix: X
spice_lib: LT1121.lib
Description: μ Power Low Dropout Regulator with Shutdown, Fixed 5V Output
Spice_Node_Sequence: [1] OUT , [2] GND , [3] _SHDN , [4] IN ,
[LT1121.asy](#)
spice_model: LT1121
Required Refdes Prefix: X
spice_lib: LT1121.lib
Description: μ Power Low Dropout Regulator with Shutdown, Adjustable Output Voltage
Spice_Node_Sequence: [1] OUT , [2] ADJ , [3] GND , [4] _SHDN , [5] IN ,
[LT1129-3.3.asy](#)
spice_model: LT1129-3.3
Required Refdes Prefix: X
spice_lib: LT1129.lib
Description: μ Power Low Dropout Regulator with Shutdown, Fixed 3.3V Output
Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] _SHDN , [5] IN ,
[LT1129-5.asy](#)
spice_model: LT1129-5
Required Refdes Prefix: X
spice_lib: LT1129.lib
Description: μ Power Low Dropout Regulator with Shutdown, Fixed 3.3V Output
Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] _SHDN , [5] IN ,
[LT1129.asy](#)
spice_model: LT1129
Required Refdes Prefix: X
spice_lib: LT1129.lib
Description: μ Power Low Dropout Regulator with Shutdown, Adjustable Output Voltage
Spice_Node_Sequence: [1] OUT , [2] ADJ , [3] GND , [4] _SHDN , [5] IN ,

[LT1158.asy](#)

spice_model: LT1158

Required Refdes Prefix: X

spice_lib: LT1158.sub

Description: Half-Bridge N-Channel Power MOSFET Driver

Spice_Node_Sequence: [1] V+ , [2] INPUT , [3] ENABLE , [4] _FAULT , [5] GND , [6] BgateFB , [7] Bgate , [8] BoostDr , [9] Tsource , [10] TgateFB , [11] Tgate , [12] Boost , [13] Sense- , [14] Sense+ ,

[LT1160.asy](#)

spice_model: LT1160

Required Refdes Prefix: X

spice_lib: LT1160.sub

Description: Half-/Full-Bridge N-Channel Power MOSFET Driver

Spice_Node_Sequence: [1] SV+ , [2] INtop , [3] INbot , [4] _UVout , [5] SGND , [6] PGND , [7] BgateFB , [8] Bgate , [9] PV+ , [10] Tsource , [11] TgateFB , [12] Tgate , [13] Boost ,

[LT1161.asy](#)

spice_model: LT1161

Required Refdes Prefix: X

spice_lib: LT1161.sub

Description: Quad Protected High-Side MOSFET Driver (2 of 4 Channels)

Spice_Node_Sequence: [1] GND , [2] Timer1 , [3] INPUT1 , [4] Timer2 , [5] INPUT2 , [6] V+ , [7] Gate2 , [8] Sense2 , [9] Gate1 , [10] Sense1 ,

[LT1162.asy](#)

spice_model: LT1162

Required Refdes Prefix: X

spice_lib: LT1160.sub

Description: Full-Bridge N-Channel Power MOSFET Driver(1 of 2 Channels)

Spice_Node_Sequence: [1] SV+ , [2] INtop , [3] INbot , [4] _UVout , [5] SGND , [6] PGND , [7] BgateFB , [8] Bgate , [9] PV+ , [10] Tsource , [11] TgateFB , [12] Tgate , [13] Boost ,

[LT1170.asy](#)

spice_model: LT1170

Required Refdes Prefix: X

spice_lib: LT1170.sub

Description: 100kHz, 5A High Efficiency Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,

[LT1170HV.asy](#)

spice_model: LT1170HV

Required Refdes Prefix: X

spice_lib: LT1170.sub

Description: 100kHz, 5A High Efficiency Switching Regulator(High Voltage version)

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,

[LT1171.asy](#)

spice_model: LT1171

Required Refdes Prefix: X

spice_lib: LT1171.sub

Description: 100kHz, 2.5A High Efficiency Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,

[LT1171HV.asy](#)

spice_model: LT1171HV

Required Refdes Prefix: X

spice_lib: LT1171.sub

Description: 100kHz, 2.5A High Efficiency Switching Regulator(High Voltage version)

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,

[LT1172.asy](#)

spice_model: LT1172

Required Refdes Prefix: X

spice_lib: LT1172.sub

Description: 100kHz, 1.25A High Efficiency Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,
[LT1172HV.asy](#)

spice_model: LT1172HV

Required Refdes Prefix: X

spice_lib: LT1172.sub

Description: 100kHz, 1.25A High Efficiency Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,
[LT1173-12.asy](#)

spice_model: LT1173-12

Required Refdes Prefix: X

spice_lib: LT1173.sub

Description: 12 Volt μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] Sense ,
[LT1173-5.asy](#)

spice_model: LT1173-5

Required Refdes Prefix: X

spice_lib: LT1173.sub

Description: 5 Volt μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] Sense ,
[LT1173.asy](#)

spice_model: LT1173

Required Refdes Prefix: X

spice_lib: LT1173.sub

Description: Adjustable μ Power DC/DC Converter

Spice_Node_Sequence: [1] Ilim , [2] Vin , [3] SW1 , [4] SW2 , [5] GND , [6] FB ,
[LT1176-5.asy](#)

spice_model: LT1176-5

Required Refdes Prefix: X

spice_lib: LT1176.sub

Description: Step-Down Switching Regulator, Fixed 5V Output

Spice_Node_Sequence: [1] Vin , [2] Ilim , [3] GND , [4] Sense , [5] _SHDN , [6] Status , [7] Vc , [8] SW ,
[LT1176.asy](#)

spice_model: LT1176

Required Refdes Prefix: X

spice_lib: LT1176.sub

Description: Step-Down Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Vin , [2] Ilim , [3] GND , [4] FB , [5] _SHDN , [6] Status , [7] Vc , [8] SW ,
[LT1182.asy](#)

spice_model: LT1182

Required Refdes Prefix: X

spice_lib: LT1182.sub

Description: CCFL/LCD Contrast Switching Regulator

Spice_Node_Sequence: [1] CCFL PGND , [2] Iccfl , [3] DIO , [4] CCFL Vc , [5] AGND , [6] _SHDN , [7] LCD Vc , [8] LCD PGND , [9] LCD SW , [10] FBN , [11] FBP , [12] Vin , [13] ROYER , [14] BAT , [15] BULB , [16] CCFL SW ,
[LT1183.asy](#)

spice_model: LT1183

Required Refdes Prefix: X

spice_lib: LT1183.sub

Description: CCFL/LCD Contrast Switching Regulator

Spice_Node_Sequence: [1] CCFL PGND , [2] Iccfl , [3] DIO , [4] CCFL Vc , [5] AGND , [6] _SHDN , [7] LCD Vc , [8] LCD PGND , [9] LCD SW , [10] FB , [11] REF , [12] Vin , [13] ROYER , [14] BAT , [15] BULB , [16] CCFL SW ,

[LT1184.asy](#)

spice_model: LT1184

Required Refdes Prefix: X

spice_lib: LT1184.sub

Description: CCFL Switching Regulator, Grounded-Lamp

Spice_Node_Sequence: [1] PGND , [2] Iccfl , [3] DIO , [4] CCFL Vc , [5] AGND , [6] _SHDN , [7] REF , [8] Vin , [9] BAT , [10] BULB , [11] CCFL SW ,

[LT1184F.asy](#)

spice_model: LT1184F

Required Refdes Prefix: X

spice_lib: LT1184F.sub

Description: CCFL Switching Regulator, Floating-Lamp or Grounded-Lamp

Spice_Node_Sequence: [1] PGND , [2] Iccfl , [3] DIO , [4] CCFL Vc , [5] AGND , [6] _SHDN , [7] REF , [8] Vin , [9] ROYER , [10] BAT , [11] BULB , [12] CCFL SW ,

[LT1186F.asy](#)

spice_model: LT1186F

Required Refdes Prefix: X

spice_lib: LT1186F.sub

Description: DAC Programmable CCFL Switching Regulator(Bits-to-Nits(TM))

Spice_Node_Sequence: [1] PGND , [2] Iccfl , [3] DIO , [4] CCFL Vc , [5] AGND , [6] _SHDN , [7] CLK , [8] _CS , [9] Din , [10] Dout , [11] Iout , [12] Vcc , [13] ROYER , [14] BAT , [15] BULB , [16] CCFL SW ,

[LT1241.asy](#)

spice_model: LT1241

Required Refdes Prefix: X

spice_lib: LT1241.sub

Description: High Speed Current Mode Pulse Width Modulator

Spice_Node_Sequence: [1] COMP , [2] FB , [3] Isense , [4] Rt/Ct , [5] GND , [6] Output , [7] Vcc , [8] Vref ,

[LT1242.asy](#)

spice_model: LT1242

Required Refdes Prefix: X

spice_lib: LT1242.sub

Description: High Speed Current Mode Pulse Width Modulator

Spice_Node_Sequence: [1] COMP , [2] FB , [3] Isense , [4] Rt/Ct , [5] GND , [6] Output , [7] Vcc , [8] Vref ,

[LT1243.asy](#)

spice_model: LT1243

Required Refdes Prefix: X

spice_lib: LT1243.sub

Description: High Speed Current Mode Pulse Width Modulator

Spice_Node_Sequence: [1] COMP , [2] FB , [3] Isense , [4] Rt/Ct , [5] GND , [6] Output , [7] Vcc , [8] Vref ,

[LT1244.asy](#)

spice_model: LT1244

Required Refdes Prefix: X

spice_lib: LT1244.sub

Description: High Speed Current Mode Pulse Width Modulator

Spice_Node_Sequence: [1] COMP , [2] FB , [3] Isense , [4] Rt/Ct , [5] GND , [6] Output , [7] Vcc , [8] Vref ,

[LT1245.asy](#)

spice_model: LT1245

Required Refdes Prefix: X

spice_lib: LT1245.sub

Description: High Speed Current Mode Pulse Width Modulator

Spice_Node_Sequence: [1] COMP , [2] FB , [3] Isense , [4] Rt/Ct , [5] GND , [6] Output , [7] Vcc , [8] Vref

[LT1246.asy](#)

spice_model: LT1246

Required Refdes Prefix: X

spice_lib: LT1246.sub

Description: 1MHz Off-Line Current Mode PWM and DC/DC Converter

Spice_Node_Sequence: [1] Comp , [2] FB , [3] Isense , [4] Rt/Ct , [5] GND , [6] Output , [7] Vcc , [8] Ref ,

[LT1247.asy](#)

spice_model: LT1247

Required Refdes Prefix: X

spice_lib: LT1247.sub

Description: 1MHz Off-Line Current Mode PWM and DC/DC Converter

Spice_Node_Sequence: [1] Comp , [2] FB , [3] Isense , [4] Rt/Ct , [5] GND , [6] Output , [7] Vcc , [8] Ref ,

[LT1248.asy](#)

spice_model: LT1248

Required Refdes Prefix: X

spice_lib: LT1248.sub

Description: Power Factor Controller\n\nNote: External sync capability is not modeled

Spice_Node_Sequence: [1] GND , [2] PKlim , [3] CAout , [4] Isense , [5] Mout , [6] Iac , [7] VAout , [8] OVP , [9] Ref , [10] Enable , [11] Vsense , [12] Rset , [13] SS , [14] Cset , [15] Vcc , [16] GTDR ,

[LT1249.asy](#)

spice_model: LT1249

Required Refdes Prefix: X

spice_lib: LT1249.sub

Description: Power Factor Controller\n\nNote: External sync capability is not modeled.

Spice_Node_Sequence: [1] GND , [2] CAout , [3] Mout , [4] Iac , [5] VAout , [6] Vsense , [7] Vcc , [8] GTDR ,

[LT1268.asy](#)

spice_model: LT1268

Required Refdes Prefix: X

spice_lib: LT1268.sub

Description: 7.5A 150kHz Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,

[LT1268B.asy](#)

spice_model: LT1268B

Required Refdes Prefix: X

spice_lib: LT1268.sub

Description: 7.5A 150kHz Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,

[LT1269.asy](#)

spice_model: LT1269

Required Refdes Prefix: X

spice_lib: LT1269.sub

Description: 4A High Efficiency Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,

[LT1270.asy](#)

spice_model: LT1270

Required Refdes Prefix: X

spice_lib: LT1270.sub

Description: 8A High Efficiency Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,
[LT1270A.asy](#)

spice_model: LT1270A

Required Refdes Prefix: X

spice_lib: LT1270A.sub

Description: 10A High Efficiency Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,
[LT1271.asy](#)

spice_model: LT1271

Required Refdes Prefix: X

spice_lib: LT1271.sub

Description: 4A High Efficiency Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin ,
[LT1300.asy](#)

spice_model: LT1300

Required Refdes Prefix: X

spice_lib: LT1300.sub

Description: μ Power High Efficiency 3.3/5V Step-Up DC/DC Converter

Spice_Node_Sequence: [1] GND , [2] Sel , [3] SHDN , [4] FB , [5] Ilim , [6] Vin , [7] SW , [8] PGND ,
[LT1301.asy](#)

spice_model: LT1301

Required Refdes Prefix: X

spice_lib: LT1301.sub

Description: μ Power High Efficiency 5V/12V DC/DC Step-Up Converter for Flash Memory

Spice_Node_Sequence: [1] GND , [2] Sel , [3] SHDN , [4] FB , [5] Ilim , [6] Vin , [7] SW , [8] PGND ,
[LT1302-5.asy](#)

spice_model: LT1302-5

Required Refdes Prefix: X

spice_lib: LT1302.sub

Description: μ Power High Output Current Step-up Fixed 5V DC/DC Converter

Spice_Node_Sequence: [1] GND , [2] Vc , [3] SHDN , [4] Sense , [5] It , [6] Vin , [7] SW , [8] PGND ,
[LT1302.asy](#)

spice_model: LT1302

Required Refdes Prefix: X

spice_lib: LT1302.sub

Description: μ Power High Output Current Step-up Adjustable DC/DC Converter

Spice_Node_Sequence: [1] GND , [2] Vc , [3] SHDN , [4] FB , [5] It , [6] Vin , [7] SW , [8] PGND ,
[LT1303-5.asy](#)

spice_model: LT1303-5

Required Refdes Prefix: X

spice_lib: LT1303.sub

Description: μ Power High Efficiency DC/DC Converter with Low-Battery Detector, Fixed 5V

Spice_Node_Sequence: [1] GND , [2] LBO , [3] SHDN , [4] Sense , [5] LBI , [6] Vin , [7] SW , [8] PGND ,

[LT1303.asy](#)

spice_model: LT1303

Required Refdes Prefix: X

spice_lib: LT1303.sub

Description: μ Power High Efficiency DC/DC Converter with Low-Battery Detector, adjustable output voltage

Spice_Node_Sequence: [1] GND , [2] LBO , [3] SHDN , [4] FB , [5] LBI , [6] Vin , [7] SW , [8] PGND , [LT1304-3.3.asy](#)

spice_model: LT1304-3.3

Required Refdes Prefix: X

spice_lib: LT1304.sub

Description: μ Power DC/DC Converter with Low-Battery Detector Active in Shutdown, Fixed 3.3V

Spice_Node_Sequence: [1] LBI , [2] LBO , [3] Vin , [4] SW , [5] GND , [6] Ilim , [7] _SHDN , [8] Sense , [LT1304-5.asy](#)

spice_model: LT1304-5

Required Refdes Prefix: X

spice_lib: LT1304.sub

Description: μ Power DC/DC Converter with Low-Battery Detector Active in Shutdown, Fixed 5V

Spice_Node_Sequence: [1] LBI , [2] LBO , [3] Vin , [4] SW , [5] GND , [6] Ilim , [7] _SHDN , [8] Sense , [LT1304.asy](#)

spice_model: LT1304

Required Refdes Prefix: X

spice_lib: LT1304.sub

Description: μ Power DC/DC Converter with Low-Battery Detector Active in Shutdown, Adjustable output voltage

Spice_Node_Sequence: [1] LBI , [2] LBO , [3] Vin , [4] SW , [5] GND , [6] Ilim , [7] _SHDN , [8] FB , [LT1305.asy](#)

spice_model: LT1305

spice_lib: LT1305.sub

Required Refdes Prefix: X

Description: μ Power High Power DC/DC Converter with Low-Battery Detector

Spice_Node_Sequence: [1] GND , [2] LBO , [3] SHDN , [4] FB , [5] LBI , [6] Vin , [7] SW , [8] PGND , [LT1306.asy](#)

spice_model: LT1306

Required Refdes Prefix: X

spice_lib: LT1306.sub

Description: Synchronous, Fixed Frequency Step-Up DC/DC Converter

Spice_Node_Sequence: [1] Vc , [2] FB , [3] OUT , [4] GND , [5] SW , [6] CAP , [7] Vin , [8] _SHDN , [LT1307.asy](#)

spice_model: LT1307

Required Refdes Prefix: X

spice_lib: LT1307.sub

Description: Single Cell μ Power 600kHz PWM DC/DC Converter

Spice_Node_Sequence: [1] Vc , [2] FB , [3] _SHDN , [4] GND , [5] SW , [6] Vin , [7] LBI , [8] LBO , [LT1307b.asy](#)

spice_model: LT1307B

Required Refdes Prefix: X

spice_lib: LT1307B.sub

Description: Single Cell μ Power 600kHz PWM DC/DC Converter

Spice_Node_Sequence: [1] Vc , [2] FB , [3] _SHDN , [4] GND , [5] SW , [6] Vin , [7] LBI , [8] LBO , [LT1308A.asy](#)

spice_model: LT1308A

Required Refdes Prefix: X

spice_lib: LT1308A.sub

Description: High Current, Single Cell μ Power 600kHz DC/DC Converter

Spice_Node_Sequence: [1] Vc , [2] FB , [3] _SHDN , [4] GND , [5] SW , [6] Vin , [7] LBI , [8] LBO , [LT1308B.asy](#)

spice_model: LT1308B

Required Refdes Prefix: X

spice_lib: LT1308B.sub

Description: High Current, Single Cell μ Power 600kHz DC/DC Converter

Spice_Node_Sequence: [1] Vc , [2] FB , [3] _SHDN , [4] GND , [5] SW , [6] Vin , [7] LBI , [8] LBO ,
[LT1309.asy](#)

spice_model: LT1309

Required Refdes Prefix: X

spice_lib: LT1309.sub

Description: 500kHz μ Power DC/DC Converter for Flash Memory

Spice_Node_Sequence: [1] SoftStart , [2] Vcc , [3] PGND , [4] SW , [5] GND , [6] _VPPOK , [7] Sense ,
[8] ON/OFF ,
[LT1310.asy](#)

spice_model: LT1310

Required Refdes Prefix: X

spice_lib: LT1310.sub

Description: 1.5A Boost DC/DC Converter with Phase-Locked Loop

Spice_Node_Sequence: [1] FB , [2] _SHDN , [3] PLL-LPF , [4] SYNC , [5] GND , [6] SW , [7] Vin , [8] Ct
, [9] Vc ,
[LT1316.asy](#)

spice_model: LT1316

Required Refdes Prefix: X

spice_lib: LT1316.sub

Description: μ Power DC/DC Converter with Programmable Peak Current Limit

Spice_Node_Sequence: [1] LBO , [2] LBI , [3] Rset , [4] GND , [5] SW , [6] Vin , [7] _SHDN , [8] FB ,
[LT1317.asy](#)

spice_model: LT1317

Required Refdes Prefix: X

spice_lib: LT1317.sub

Description: μ Power, 600kHz PWM DC/DC Converter

Spice_Node_Sequence: [1] Vc , [2] FB , [3] _SHDN , [4] GND , [5] SW , [6] Vin , [7] LBI , [8] LBO ,
[LT1317b.asy](#)

spice_model: LT1317B

Required Refdes Prefix: X

spice_lib: LT1317B.sub

Description: μ Power, 600kHz PWM DC/DC Converter

Spice_Node_Sequence: [1] Vc , [2] FB , [3] _SHDN , [4] GND , [5] SW , [6] Vin , [7] LBI , [8] LBO ,
[LT1336.asy](#)

spice_model: LT1336

Required Refdes Prefix: X

spice_lib: LT1336.sub

Description: Half-Bridge N-Channel Power MOSFET Driver with Boost Regulator

Spice_Node_Sequence: [1] Isense , [2] SV+ , [3] InTop , [4] InBottom , [5] _UVOUT , [6] SGND , [7]
PGND , [8] BgateFB , [9] BgateDR , [10] PV+ , [11] Tsource , [12] TgateFB , [13] TgateDR , [14] Boost ,
[15] SWGND , [16] Switch ,
[LT1339.asy](#)

spice_model: LT1339

Required Refdes Prefix: X

spice_lib: LT1339.sub

Description: High Power Synchronous DC/DC Controller

Spice_Node_Sequence: [1] 5Vref , [2] Ct , [3] SL/ADJ , [4] Iave , [5] SS , [6] Vc , [7] SGND , [8] FB , [9]
Sense+ , [10] Sense- , [11] Run/SHDN , [12] Phase , [13] PGND , [14] BG , [15] 12Vin , [16] TS , [17] TG ,
[18] Boost ,
[LT1370.asy](#)

spice_model: LT1370

Required Refdes Prefix: X

spice_lib: LT1370.sub

Description: 500kHz High Efficiency 6A Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] _SHDN , [4] GND , [5] SW , [6] NFB , [7] Vin ,
[LT1370HV.asy](#)

spice_model: LT1370HV

Required Refdes Prefix: X

spice_lib: LT1370.sub

Description: 500kHz High Efficiency 6A Switching Regulator, High Voltage Version

Spice_Node_Sequence: [1] Vc , [2] FB , [3] _SHDN , [4] GND , [5] SW , [6] NFB , [7] Vin ,
[LT1371.asy](#)

spice_model: LT1371

Required Refdes Prefix: X

spice_lib: LT1371.sub

Description: 500kHz High Efficiency 3A Switching Regulator

Spice_Node_Sequence: [1] Vc , [2] FB , [3] NFB , [4] GND , [5] SW , [6] _SHDN , [7] Vin ,
[LT1371HV.asy](#)

spice_model: LT1371HV

Required Refdes Prefix: X

spice_lib: LT1371.sub

Description: 500kHz High Efficiency 3A Switching Regulator(High Voltage version)

Spice_Node_Sequence: [1] Vc , [2] FB , [3] NFB , [4] GND , [5] SW , [6] _SHDN , [7] Vin ,
[LT1372.asy](#)

spice_model: LT1372

Required Refdes Prefix: X

spice_lib: LT1372.sub

Description: 1.5A, 500kHz High Efficiency Step-Down Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Vc , [2] FB , [3] NFB , [4] _SHDN , [5] Vin , [6] GND , [7] SW ,
[LT1372HV.asy](#)

spice_model: LT1372HV

Required Refdes Prefix: X

spice_lib: LT1372.sub

Description: 1.5A, 500kHz High Efficiency Step-Down Switching Regulator, Adjustable Output Voltage(High Voltage version)

Spice_Node_Sequence: [1] Vc , [2] FB , [3] NFB , [4] _SHDN , [5] Vin , [6] GND , [7] SW ,
[LT1373.asy](#)

spice_model: LT1373

Required Refdes Prefix: X

spice_lib: LT1373.sub

Description: 250kHz Low Supply Current High Efficiency 1.5A Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Vc , [2] FB , [3] NFB , [4] _SHDN , [5] Vin , [6] SGND , [7] PGND , [8] SW ,
[LT1373HV.asy](#)

spice_model: LT1373HV

Required Refdes Prefix: X

spice_lib: LT1373.sub

Description: 250kHz Low Supply Current High Efficiency 1.5A Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Vc , [2] FB , [3] NFB , [4] _SHDN , [5] Vin , [6] SGND , [7] PGND , [8] SW ,
[LT1374-5.asy](#)

spice_model: LT1374-5

Required Refdes Prefix: X

spice_lib: LT1374.sub

Description: 4.5A, 500KHz Step-Down Switching Regulator, Fixed 5Volt Output

Spice_Node_Sequence: [1] Vin , [2] Boost , [3] Sense , [4] GND , [5] BIAS , [6] Vc , [7] _SHDN , [8] SW ,

[LT1374.asy](#)

spice_model: LT1374

Required Refdes Prefix: X

spice_lib: LT1374.sub

Description: 4.5A, 500KHz Step-Down Switching Regulator

Spice_Node_Sequence: [1] Vin , [2] Boost , [3] FB , [4] GND , [5] BIAS , [6] Vc , [7] _SHDN , [8] SW ,

[LT1374HV-5.asy](#)

spice_model: LT1374HV-5

Required Refdes Prefix: X

spice_lib: LT1374.sub

Description: 4.5A, 500KHz Step-Down Switching Regulator, Fixed 5Volt Output

Spice_Node_Sequence: [1] Vin , [2] Boost , [3] Sense , [4] GND , [5] BIAS , [6] Vc , [7] _SHDN , [8] SW ,

[LT1374HV.asy](#)

spice_model: LT1374HV

Required Refdes Prefix: X

spice_lib: LT1374.sub

Description: 4.5A, 500KHz Step-Down Switching Regulator, High Voltage Version

Spice_Node_Sequence: [1] Vin , [2] Boost , [3] FB , [4] GND , [5] BIAS , [6] Vc , [7] _SHDN , [8] SW ,

[LT1375-5.asy](#)

spice_model: LT1375-5

Required Refdes Prefix: X

spice_lib: LT1375.sub

Description: 1.5A, 500kHz Step-Down Switching Regulator, Fixed 5V Output

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] _SHDN , [5] GND , [6] Sense , [7] Vc ,

[LT1375.asy](#)

spice_model: LT1375

Required Refdes Prefix: X

spice_lib: LT1375.sub

Description: 1.5A, 500kHz Step-Down Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] _SHDN , [5] GND , [6] FB , [7] Vc ,

[LT1375HV.asy](#)

spice_model: LT1375HV

Required Refdes Prefix: X

spice_lib: LT1375.sub

Description: 1.5A, 500kHz Step-Down Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] _SHDN , [5] GND , [6] FB , [7] Vc ,

[LT1376-5.asy](#)

spice_model: LT1376-5

Required Refdes Prefix: X

spice_lib: LT1376.sub

Description: 1.5A, 500kHz Step-Down Switching Regulator, Fixed 5V Output

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] BIAS , [5] _SHDN , [6] GND , [7] Sense , [8] Vc ,

[LT1376.asy](#)

spice_model: LT1376

Required Refdes Prefix: X

spice_lib: LT1376.sub

Description: 1.5A, 500kHz Step-Down Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] BIAS , [5] _SHDN , [6] GND , [7] FB , [8] Vc ,

[LT1376HV.asy](#)

spice_model: LT1376HV

Required Refdes Prefix: X

spice_lib: LT1376.sub

Description: 1.5A, 500kHz Step-Down Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] BIAS , [5] _SHDN , [6] GND , [7] FB , [8] Vc ,
[LT1377.asy](#)

spice_model: LT1377

Required Refdes Prefix: X

spice_lib: LT1377.sub

Description: 1.5A, 1MHz High Efficiency Step-Down Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Vc , [2] FB , [3] NFB , [4] _SHDN , [5] Vin , [6] GND , [7] SW ,
[LT1424-5.asy](#)

spice_model: LT1424-5

Required Refdes Prefix: X

spice_lib: LT1424-5.sub

Description: Isolated Flyback Switching Regulator with 5V Output

Spice_Node_Sequence: [1] _SHDN , [2] Vc , [3] SGND , [4] PGND , [5] SW , [6] Vin , [7] Rcomp ,
[LT1424-9.asy](#)

spice_model: LT1424-9

Required Refdes Prefix: X

spice_lib: LT1424-9.sub

Description: Isolated Flyback Switching Regulator with 9V Output

Spice_Node_Sequence: [1] _SHDN , [2] Vc , [3] SGND , [4] PGND , [5] SW , [6] Vin , [7] Rcomp ,
[LT1425.asy](#)

spice_model: LT1425

Required Refdes Prefix: X

spice_lib: LT1425.sub

Description: Isolated Flyback Switching Regulator

Spice_Node_Sequence: [1] Rfb , [2] Vc , [3] Rref , [4] SGND , [5] PGND , [6] SW , [7] Vin , [8] Rcomp ,
[9] Rocomp , [10] _SHDN ,
[LT1432-3.3.asy](#)

spice_model: LT1432-3.3

Required Refdes Prefix: X

spice_lib: LT1432-3.3.sub

Description: 3.3V High Efficiency Step-Down Switching Regulator Controller

Spice_Node_Sequence: [1] Vlim , [2] Vout , [3] Vin , [4] V+ , [5] Diode , [6] Vc , [7] GND , [8] Mode ,
[LT1432.asy](#)

spice_model: LT1432

Required Refdes Prefix: X

spice_lib: LT1432.sub

Description: 5V High Efficiency Step-Down Switching Regulator Controller

Spice_Node_Sequence: [1] Vlim , [2] Vout , [3] Vin , [4] V+ , [5] Diode , [6] Vc , [7] GND , [8] Mode ,
[LT1500-3.asy](#)

spice_model: LT1500-3/5

Required Refdes Prefix: X

spice_lib: LT1500-3.sub

Description: Adaptive-Frequency Current-Mode Switching Regulator, Selectable 3.3V or 5V Output

Spice_Node_Sequence: [1] _SHDN , [2] Vc , [3] Vin , [4] Isense , [5] GND , [6] PGND , [7] SW , [8]
SYNC , [9] LBO , [10] LBI , [11] SS , [12] SEL3.3/5 , [13] Sense ,
[LT1500.asy](#)

spice_model: LT1500

Required Refdes Prefix: X

spice_lib: LT1500.sub

Description: Adaptive-Frequency Current-Mode Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] _SHDN , [2] Vc , [3] Vin , [4] Isense , [5] GND , [6] PGND , [7] SW , [8] SYNC , [9] LBO , [10] LBI , [11] SS , [12] NFB , [13] FB ,

[LT1501-3.3.asy](#)

spice_model: LT1501-3.3

Required Refdes Prefix: X

spice_lib: LT1501-3.3.sub

Description: Adaptive-Frequency Current-Mode Switching Regulator, Fixed 3.3V Output

Spice_Node_Sequence: [1] _SHDN , [2] Vin , [3] Isense , [4] GND , [5] SW , [6] LBO , [7] LBI , [8] FB ,

[LT1501-5.asy](#)

spice_model: LT1501-5

Required Refdes Prefix: X

spice_lib: LT1501-5.sub

Description: Adaptive-Frequency Current-Mode Switching Regulator, Fixed 5V Output

Spice_Node_Sequence: [1] _SHDN , [2] Vin , [3] Isense , [4] GND , [5] SW , [6] LBO , [7] LBI , [8] FB ,

[LT1501.asy](#)

spice_model: LT1501

Required Refdes Prefix: X

spice_lib: LT1501.sub

Description: Adaptive-Frequency Current-Mode Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] _SHDN , [2] Vin , [3] Isense , [4] GND , [5] SW , [6] LBO , [7] LBI , [8] FB ,

[LT1505-1.asy](#)

spice_model: LT1505-1

Required Refdes Prefix: X

spice_lib: LT1505-1.sub

Description: Constant-Current/Voltage High Efficiency Battery Charger

Spice_Node_Sequence: [1] Boost , [2] Tgate , [3] SW , [4] _SHDN , [5] AGND , [6] UVLO , [7] INFET , [8] GND , [9] CAP , [10] _FLAG , [11] 4.1V , [12] 4.2V , [13] 3 Cell , [14] FB , [15] Vc , [16] Prog , [17] BAT2 , [18] Sense , [19] SPIN , [20] BAT , [21] Vcc , [22] BoostC , [23] Gbias , [24] Bgate , [25] PGND ,

[LT1505.asy](#)

spice_model: LT1505

Required Refdes Prefix: X

spice_lib: LT1505.sub

Description: Constant-Current/Voltage High Efficiency Battery Charger

Spice_Node_Sequence: [1] Boost , [2] Tgate , [3] SW , [4] _SHDN , [5] AGND , [6] UVLO , [7] INFET , [8] CLP , [9] CLN , [10] COMP1 , [11] CAP , [12] _FLAG , [13] 4.1V , [14] 4.2V , [15] 3 Cell , [16] FB , [17] Vc , [18] Prog , [19] BAT2 , [20] Sense , [21] SPIN , [22] BAT , [23] Vcc , [24] BoostC , [25] Gbias , [26] Bgate , [27] PGND ,

[LT1506-3.3.asy](#)

spice_model: LT1506-3.3

Required Refdes Prefix: X

spice_lib: LT1506.sub

Description: 4.5A, 500KHz Step-Down Switching Regulator, Fixed 3.3V Output

Spice_Node_Sequence: [1] Vin , [2] Boost , [3] Sense , [4] GND , [5] Vc , [6] _SHDN , [7] SW ,

[LT1506.asy](#)

spice_model: LT1506

Required Refdes Prefix: X

spice_lib: LT1506.sub

Description: 4.5A, 500KHz Step-Down Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Vin , [2] Boost , [3] FB , [4] GND , [5] Vc , [6] _SHDN , [7] SW ,

[LT1507-3.3.asy](#)

spice_model: LT1507-3.3

Required Refdes Prefix: X

spice_lib: LT1507.sub

Description: 500kHz Monolithic Buck Mode Switching Regulator, Fixed 3.3V Output Voltage

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] _SHDN , [5] GND , [6] Sense , [7] Vc ,
[LT1507.asy](#)

spice_model: LT1507

Required Refdes Prefix: X

spice_lib: LT1507.sub

Description: 500kHz Monolithic Buck Mode Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] _SHDN , [5] GND , [6] FB , [7] Vc ,
[LT1508.asy](#)

spice_model: LT1508

Required Refdes Prefix: X

spice_lib: LT1508.sub

Description: Power Factor and PWM Controller(Voltage Mode)

Spice_Node_Sequence: [1] GTDR1 , [2] GND2 , [3] GND1 , [4] Cset , [5] PKlim , [6] CAout , [7] Isense ,
[8] Mout , [9] Iac , [10] VAout , [11] OVP , [12] REF , [13] SS2 , [14] Vsense , [15] Rset , [16] SS1 , [17]
Vcc , [18] Vc , [19] Ilim , [20] GTDR2 ,
[LT1509.asy](#)

spice_model: LT1509

Required Refdes Prefix: X

spice_lib: LT1509.sub

Description: Power Factor and PWM Controller

Spice_Node_Sequence: [1] GTDR1 , [2] GND2 , [3] GND1 , [4] Cset , [5] PKlim , [6] CAout , [7] Isense ,
[8] Mout , [9] Iac , [10] VAout , [11] OVP , [12] REF , [13] SS2 , [14] Vsense , [15] Rset , [16] SS1 , [17]
Vcc , [18] Vc , [19] RAMP , [20] GTDR2 ,
[LT1510-5.asy](#)

spice_model: LT1510-5

Required Refdes Prefix: X

spice_lib: LT1510-5.sub

Description: 500KHz Constant-Voltage/Constant-Current Battery Charger

Spice_Node_Sequence: [1] GND , [2] SW , [3] Boost , [4] OVP , [5] Sense , [6] BAT , [7] Vc , [8] Prog ,
[9] Vcc ,
[LT1510.asy](#)

spice_model: LT1510

Required Refdes Prefix: X

spice_lib: LT1510.sub

Description: Constant-Voltage/Constant-Current Battery Charger

Spice_Node_Sequence: [1] GND , [2] SW , [3] Boost , [4] OVP , [5] Sense , [6] BAT , [7] Vc , [8] Prog ,
[9] Vcc ,
[LT1512.asy](#)

spice_model: LT1512

Required Refdes Prefix: X

spice_lib: LT1512.sub

Description: SEPIC Constant-Current/Constant Voltage Battery Charger

Spice_Node_Sequence: [1] Vc , [2] FB , [3] IFB , [4] _SHDN , [5] Vin , [6] SGND , [7] PGND , [8] SW ,
[LT1513-2.asy](#)

spice_model: LT1513-2

Required Refdes Prefix: X

spice_lib: LT1513-2.sub

Description: SEPIC Constant- or Constant Current/Constant Voltage Battery Charger

Spice_Node_Sequence: [1] Vc , [2] FB , [3] IFB , [4] GND , [5] SW , [6] _SHDN , [7] Vin ,
[LT1513.asy](#)

spice_model: LT1513

Required Refdes Prefix: X

spice_lib: LT1513.sub

Description: SEPIC Constant- or Constant Current/Constant Voltage Battery Charger

Spice_Node_Sequence: [1] Vc , [2] FB , [3] IFB , [4] GND , [5] SW , [6] _SHDN , [7] Vin ,
[LT1521-3.3.asy](#)

spice_model: LT1521-3.3

Required Refdes Prefix: X

spice_lib: LT1521.lib

Description: 300mA Low Dropout Regulator with μ Power Quiescent Current and Shutdown, Fixed 3.3V Output

Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] _SHDN , [5] IN ,
[LT1521-3.asy](#)

spice_model: LT1521-3

Required Refdes Prefix: X

spice_lib: LT1521.lib

Description: 300mA Low Dropout Regulator with μ Power Quiescent Current and Shutdown, Fixed 3V Output

Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] _SHDN , [5] IN ,
[LT1521-5.asy](#)

spice_model: LT1521-5

Required Refdes Prefix: X

spice_lib: LT1521.lib

Description: 300mA Low Dropout Regulator with μ Power Quiescent Current and Shutdown, Fixed 5V Output

Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] _SHDN , [5] IN ,
[LT1521.asy](#)

spice_model: LT1521

Required Refdes Prefix: X

spice_lib: LT1521.lib

Description: 300mA Low Dropout Regulator with μ Power Quiescent Current and Shutdown, Adjustable Output Voltage

Spice_Node_Sequence: [1] OUT , [2] ADJ , [3] GND , [4] _SHDN , [5] IN ,
[LT1528.asy](#)

spice_model: LT1528

Required Refdes Prefix: X

spice_lib: LT1528.lib

Description: 3A Low Dropout Regulator for μ Processor Applications

Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] _SHDN , [5] IN ,
[LT1529-3.3.asy](#)

spice_model: LT1529-3.3

Required Refdes Prefix: X

spice_lib: LT1529.lib

Description: 3A Low Dropout Regulator with μ Power Quiescent Current and Shutdown, Fixed 3.3V Output

Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] _SHDN , [5] Vin ,
[LT1529-5.asy](#)

spice_model: LT1529-5

Required Refdes Prefix: X

spice_lib: LT1529.lib

Description: 3A Low Dropout Regulator with μ Power Quiescent Current and Shutdown, Fixed 5V Output

Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] _SHDN , [5] Vin ,
[LT1529.asy](#)

spice_model: LT1529

Required Refdes Prefix: X

spice_lib: LT1529.lib

Description: 3A Low Dropout Regulator with μ Power Quiescent Current and Shutdown, Adjustable Output Voltage

Spice_Node_Sequence: [1] OUT , [2] ADJ , [3] GND , [4] _SHDN , [5] Vin ,

[LT1533.asy](#)

spice_model: LT1533

Required Refdes Prefix: X

spice_lib: LT1533.sub

Description: Ultralow Noise 1A Switching Regulator

Spice_Node_Sequence: [1] COLB , [2] Ct , [3] Rt , [4] FB , [5] NFB , [6] GND , [7] Vc , [8] _SHDN , [9] RCSL , [10] RVSL , [11] Vin , [12] COLA , [13] PGND ,

[LT1534-1.asy](#)

spice_model: LT1534-1

Required Refdes Prefix: X

spice_lib: LT1534-1.sub

Description: Ultralow Noise 2A Switching Regulator

Spice_Node_Sequence: [1] COL , [2] Ct , [3] Rt , [4] FB , [5] NFB , [6] GND , [7] Vc , [8] _SHDN , [9] RCSL , [10] RVSL , [11] Vin , [12] PGND ,

[LT1534.asy](#)

spice_model: LT1534

Required Refdes Prefix: X

spice_lib: LT1534.sub

Description: Ultralow Noise 2A Switching Regulator

Spice_Node_Sequence: [1] COL , [2] Ct , [3] Rt , [4] FB , [5] NFB , [6] GND , [7] Vc , [8] _SHDN , [9] RCSL , [10] RVSL , [11] Vin , [12] COL , [13] PGND ,

[LT1572.asy](#)

spice_model: LT1572

Required Refdes Prefix: X

spice_lib: LT1572.sub

Description: 100kHz, 1.25A High Efficiency Switching Regulator with Catch Diode

Spice_Node_Sequence: [1] Vc , [2] FB , [3] GND , [4] SW , [5] Vin , [6] Anode , [7] Cathode ,

[LT1573.asy](#)

spice_model: LT1573

Required Refdes Prefix: X

spice_lib: LT1573.lib

Description: Low Dropout PNP Regulator Driver

Spice_Node_Sequence: [1] FB , [2] Latch , [3] SHDN , [4] GND , [5] Drive , [6] Vin , [7] Vout , [8] Comp ,

[LT1575.asy](#)

spice_model: LT1575

Required Refdes Prefix: X

spice_lib: LT1575.sub

Description: Ultrafast Transient Response, Low Dropout Regulator, Adjustable Output Voltage
Note:
The underlying model for this part only models the device's basic logic and connectivity. The specifics of its dynamic behavior are not modeled.

Spice_Node_Sequence: [1] SHDN , [2] Vin , [3] GND , [4] FB , [5] Comp , [6] Gate , [7] Ineg , [8] Ipos ,

[LT1576-5.asy](#)

spice_model: LT1576-5

Required Refdes Prefix: X

spice_lib: LT1576.sub

Description: 1.5A, 200kHz Step-Down Switching Regulator, Fixed 5V Output

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] BIAS , [5] _SHDN , [6] GND , [7] Sense , [8] Vc ,

[LT1576.asy](#)

spice_model: LT1576

Required Refdes Prefix: X

spice_lib: LT1576.sub

Description: 1.5A, 200kHz Step-Down Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] BIAS , [5] _SHDN , [6] GND , [7] FB , [8] Vc ,

[LT1578-2.5.asy](#)

spice_model: LT1578-2.5

Required Refdes Prefix: X

spice_lib: LT1578-2.5.sub

Description: 1.5A, 200kHz Step-Down Switching Regulator, Fixed 2.5V Output

Spice_Node_Sequence: [1] SW , [2] Vin , [3] Boost , [4] GND , [5] Vc , [6] Sense , [7] _SHDN ,

[LT1578.asy](#)

spice_model: LT1578

Required Refdes Prefix: X

spice_lib: LT1578.sub

Description: 1.5A, 200kHz Step-Down Switching Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] SW , [2] Vin , [3] Boost , [4] GND , [5] Vc , [6] FB , [7] _SHDN ,

[LT1584-3.3.asy](#)

spice_model: LT1584-3.3

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 7A Low Dropout Fast Response Positive Regulator, Fixed 3.3V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LT1584-3.38.asy](#)

spice_model: LT1584-3.38

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 7A Low Dropout Fast Response Positive Regulator, Fixed 3.38V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LT1584-3.45.asy](#)

spice_model: LT1584-3.45

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 7A Low Dropout Fast Response Positive Regulator, Fixed 3.45V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LT1584-3.6.asy](#)

spice_model: LT1584-3.6

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 7A Low Dropout Fast Response Positive Regulator, Fixed 3.6V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LT1584.asy](#)

spice_model: LT1584

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 7A Low Dropout Fast Response Positive Adjustable Regulator

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LT1585-3.3.asy](#)

spice_model: LT1585-3.3

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 4.6A Low Dropout Fast Response Positive Regulator, Fixed 3.3V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LT1585-3.38.asy](#)

spice_model: LT1585-3.38

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 4.6A Low Dropout Fast Response Positive Regulator, Fixed 3.38V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LT1585-3.45.asy](#)

spice_model: LT1585-3.45

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 4.6A Low Dropout Fast Response Positive Regulator, Fixed 3.45V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LT1585-3.6.asy](#)

spice_model: LT1585-3.6

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 4.6A Low Dropout Fast Response Positive Regulator, Fixed 3.6V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LT1585.asy](#)

spice_model: LT1585

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 4.6A Low Dropout Fast Response Positive Adjustable Regulator

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LT1587-3.3.asy](#)

spice_model: LT1587-3.3

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 4.6A Low Dropout Fast Response Positive Regulator, Fixed 3.3V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LT1587-3.38.asy](#)

spice_model: LT1587-3.38

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 4.6A Low Dropout Fast Response Positive Regulator, Fixed 3.38V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LT1587-3.45.asy](#)

spice_model: LT1587-3.45

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 4.6A Low Dropout Fast Response Positive Regulator, Fixed 3.45V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LT1587-3.6.asy](#)

spice_model: LT1587-3.6

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 4.6A Low Dropout Fast Response Positive Regulator, Fixed 3.6V Output

Spice_Node_Sequence: [1] GND , [2] OUT , [3] IN ,

[LT1587.asy](#)

spice_model: LT1587

Required Refdes Prefix: X

spice_lib: LT1584.lib

Description: 3A Low Dropout Fast Response Positive Adjustable Regulator

Spice_Node_Sequence: [1] ADJ , [2] OUT , [3] IN ,

[LT1610.asy](#)

spice_model: LT1610

Required Refdes Prefix: X

spice_lib: LT1610.sub

Description: 1.7MHz, Single Cell μ Power DC/DC Converter

Spice_Node_Sequence: [1] Vc , [2] FB , [3] _SHDN , [4] PGND , [5] SW , [6] Vin , [7] GND , [8] COMP ,

[LT1611.asy](#)

spice_model: LT1611

Required Refdes Prefix: X

spice_lib: LT1611.sub

Description: Inverting 1.4MHz Switching Regulator in SOT-23

Spice_Node_Sequence: [1] SW , [2] GND , [3] NFB , [4] _SHDN , [5] Vin ,

[LT1612.asy](#)

spice_model: LT1612

Required Refdes Prefix: X

spice_lib: LT1612.sub

Description: Synchronous, Step-Down 800KHz PWM DC/DC Converter

Spice_Node_Sequence: [1] Vc , [2] FB , [3] Vin , [4] GND , [5] SW , [6] Boost , [7] _Mode , [8] _SHDN ,

[LT1613.asy](#)

spice_model: LT1613

Required Refdes Prefix: X

spice_lib: LT1613.sub

Description: 1.4MHz, Single Cell DC/DC Converter in 5-Lead SOT-23

Spice_Node_Sequence: [1] SW , [2] GND , [3] FB , [4] _SHDN , [5] Vin ,

[LT1614.asy](#)

spice_model: LT1614

Required Refdes Prefix: X

spice_lib: LT1614.sub

Description: Inverting 600kHz Switching Regulator

Spice_Node_Sequence: [1] NFB , [2] Vc , [3] _SHDN , [4] GND , [5] SW , [6] Vin , [7] LBI , [8] LBO ,

[LT1615-1.asy](#)

spice_model: LT1615-1

Required Refdes Prefix: X

spice_lib: LT1615-1.sub

Description: μ Power Step-Up DC/DC Converter in SOT-23

Spice_Node_Sequence: [1] SW , [2] GND , [3] FB , [4] _SHDN , [5] Vin ,

[LT1615.asy](#)

spice_model: LT1615

Required Refdes Prefix: X

spice_lib: LT1615.sub

Description: μ Power Step-Up DC/DC Converter in SOT-23

Spice_Node_Sequence: [1] SW , [2] GND , [3] FB , [4] _SHDN , [5] Vin ,

[LT1616.asy](#)

spice_model: LT1616

Required Refdes Prefix: X

spice_lib: LT1616.sub

Description: 600mA, 1.4MHz Step-Down Switching Regulator in SOT-23

Spice_Node_Sequence: [1] Boost , [2] GND , [3] FB , [4] _SHDN , [5] Vin , [6] SW ,

[LT1617-1.asy](#)

spice_model: LT1617-1

Required Refdes Prefix: X

spice_lib: LT1617-1.sub

Description: μ Power Inverting DC/DC Converters in SOT-23

Spice_Node_Sequence: [1] SW , [2] GND , [3] NFB , [4] _SHDN , [5] Vin ,
[LT1617.asy](#)

spice_model: LT1617

Required Refdes Prefix: X

spice_lib: LT1617.sub

Description: μ Power Inverting DC/DC Converters in SOT-23

Spice_Node_Sequence: [1] SW , [2] GND , [3] NFB , [4] _SHDN , [5] Vin ,
[LT1618.asy](#)

spice_model: LT1618

Required Refdes Prefix: X

spice_lib: LT1618.sub

Description: Constant-Current/Constant-Voltage 1.4MHz Step-Up DC/DC Converter

Spice_Node_Sequence: [1] FB , [2] ISN , [3] ISP , [4] Iadj , [5] GND , [6] SW , [7] Vin , [8] _SHDN , [9] Vc ,
[LT1619.asy](#)

spice_model: LT1619

spice_lib: LT1619.sub

Required Refdes Prefix: X

Description: Low Voltage Current Mode PWM Controller

Spice_Node_Sequence: [1] _SHDN , [2] FB , [3] VC , [4] GND , [5] Sense , [6] Gate , [7] DRV , [8] Vin ,
[LT1620.asy](#)

spice_model: LT1620

Required Refdes Prefix: X

spice_lib: LT1620.sub

Description: Rail-to-Rail Current Sense Amplifier

Spice_Node_Sequence: [1] Sense , [2] Iout , [3] GND , [4] Mode , [5] IN- , [6] IN+ , [7] Vcc , [8] AVE2 , [9] PROG2 , [10] PROG , [11] AVE ,
[LT1620S8.asy](#)

spice_model: LT1620S8

Required Refdes Prefix: X

spice_lib: LT1620S8.sub

Description: Rail-to-Rail Current Sense Amplifier

Spice_Node_Sequence: [1] Sense , [2] Iout , [3] GND , [4] IN- , [5] IN+ , [6] Vcc , [7] PROG , [8] AVE ,
[LT1621.asy](#)

spice_model: LT1621

Required Refdes Prefix: X

spice_lib: LT1621.sub

Description: Dual Rail-to-Rail Current Sense Amplifier

Spice_Node_Sequence: [1] PROG A , [2] AVE A , [3] Sense A , [4] Iout A , [5] GND B , [6] IN- B , [7] IN+ B , [8] Vcc B , [9] PROG B , [10] AVE B , [11] Sense B , [12] Iout B , [13] GND A , [14] IN- A , [15] IN+ A , [16] Vcc A ,
[LT1676.asy](#)

spice_model: LT1676

Required Refdes Prefix: X

spice_lib: LT1676.sub

Description: Wide Input Range, High Efficiency, Step-Down Switching Regulator

Spice_Node_Sequence: [1] _SHDN , [2] Vcc , [3] SW , [4] GND , [5] Vin , [6] FB , [7] Vc ,
[LT1680.asy](#)

spice_model: LT1680

Required Refdes Prefix: X

spice_lib: LT1680.sub

Description: High Power DC/DC Step-Up Controller

Spice_Node_Sequence: [1] SL/ADJ , [2] Ct , [3] Iave , [4] SS , [5] Vc , [6] SGND , [7] FB , [8] Sense+ , [9] Sense- , [10] Run/SHDN , [11] PGND , [12] GATE , [13] 12Vin , [14] 5Vref ,

[LT1681.asy](#)

spice_model: LT1681

Required Refdes Prefix: X

spice_lib: LT1681.sub

Description: Dual Transistor Synchronous Forward Controller

Spice_Node_Sequence: [1] _SHDN , [2] OVLO , [3] TERM , [4] GSND , [5] 5Vref , [6] FSET , [7] SS , [8] FB , [9] Vc , [10] Sense , [11] Imax , [12] SG , [13] Vcc , [14] PGND , [15] GB , [16] BLKSENS , [17] BSTREF , [18] TG , [19] Vbst ,

[LT1683.asy](#)

spice_model: LT1683

Required Refdes Prefix: X

spice_lib: LT1683.sub

Description: Slew Rate Controlled Ultralow Noise Push-Pull DC/DC Controller

Spice_Node_Sequence: [1] GateA , [2] CapA , [3] GCL , [4] CS , [5] V5 , [6] Ct , [7] Rt , [8] FB , [9] NFB , [10] GND , [11] Vc , [12] SS , [13] _SHDN , [14] RCSL , [15] RVSL , [16] Vin , [17] CapB , [18] GateB , [19] PGND ,

[LT1725.asy](#)

spice_model: LT1725

Required Refdes Prefix: X

spice_lib: LT1725.sub

Description: General Purpose Isolated Flyback Controller

Spice_Node_Sequence: [1] PGND , [2] Isense , [3] SFST , [4] Rocmp , [5] Rcmpe , [6] OSCAP , [7] Vc , [8] FB , [9] 3Vout , [10] UVLO , [11] SGND , [12] MinEnab , [13] EnDly , [14] Ton , [15] Vcc , [16] GATE

,

[LT1737.asy](#)

spice_model: LT1737

Required Refdes Prefix: X

spice_lib: LT1737.sub

Description: High Power Isolated Flyback Controller

Spice_Node_Sequence: [1] PGND , [2] Isense , [3] SFST , [4] Rocmp , [5] Rcmpe , [6] OSCAP , [7] Vc , [8] FB , [9] 3Vout , [10] UVLO , [11] SGND , [12] MinEnab , [13] EnDly , [14] Ton , [15] Vcc , [16] GATE

,

[LT1738.asy](#)

spice_model: LT1738

Required Refdes Prefix: X

spice_lib: LT1738.sub

Description: Slew Rate Controlled Ultralow Noise DC/DC Controller

Spice_Node_Sequence: [1] Gate , [2] Cap , [3] GCL , [4] CS , [5] V5 , [6] Ct , [7] Rt , [8] FB , [9] NFB , [10] GND , [11] Vc , [12] SS , [13] _SHDN , [14] RCSL , [15] RVSL , [16] Vin , [17] PGND ,

[LT1761-1.2.asy](#)

spice_model: LT1761-1.2

Required Refdes Prefix: X

spice_lib: LT1761.lib

Description: 100mA Low Noise Low Dropout μ Power Regulator with Shutdown in SOT-23, 1.2V Output

Spice_Node_Sequence: [1] IN , [2] GND , [3] _SHDN , [4] BYP , [5] OUT ,

[LT1761-1.5.asy](#)

spice_model: LT1761-1.5

Required Refdes Prefix: X

spice_lib: LT1761.lib

Description: 100mA Low Noise Low Dropout μ Power Regulator with Shutdown in SOT-23, 1.5V Output

Spice_Node_Sequence: [1] IN , [2] GND , [3] _SHDN , [4] BYP , [5] OUT ,

[LT1761-1.8.asy](#)

spice_model: LT1761-1.8

Required Refdes Prefix: X

spice_lib: LT1761.lib

Description: 100mA Low Noise Low Dropout μ Power Regulator with Shutdown in SOT-23, 1.8V Output

Spice_Node_Sequence: [1] IN , [2] GND , [3] _SHDN , [4] BYP , [5] OUT ,

[LT1761-2.5.asy](#)

spice_model: LT1761-2.5

Required Refdes Prefix: X

spice_lib: LT1761.lib

Description: 100mA Low Noise Low Dropout μ Power Regulator with Shutdown in SOT-23, 2.5V Output

Spice_Node_Sequence: [1] IN , [2] GND , [3] _SHDN , [4] BYP , [5] OUT ,

[LT1761-2.8.asy](#)

spice_model: LT1761-2.8

Required Refdes Prefix: X

spice_lib: LT1761.lib

Description: 100mA Low Noise Low Dropout μ Power Regulator with Shutdown in SOT-23, 2.8V Output

Spice_Node_Sequence: [1] IN , [2] GND , [3] _SHDN , [4] BYP , [5] OUT ,

[LT1761-2.asy](#)

spice_model: LT1761-2

Required Refdes Prefix: X

spice_lib: LT1761.lib

Description: 100mA Low Noise Low Dropout μ Power Regulator with Shutdown in SOT-23, 2V Output

Spice_Node_Sequence: [1] IN , [2] GND , [3] _SHDN , [4] BYP , [5] OUT ,

[LT1761-3.3.asy](#)

spice_model: LT1761-3.3

Required Refdes Prefix: X

spice_lib: LT1761.lib

Description: 100mA Low Noise Low Dropout μ Power Regulator with Shutdown in SOT-23, 3.3V Output

Spice_Node_Sequence: [1] IN , [2] GND , [3] _SHDN , [4] BYP , [5] OUT ,

[LT1761-3.asy](#)

spice_model: LT1761-3

Required Refdes Prefix: X

spice_lib: LT1761.lib

Description: 100mA Low Noise Low Dropout μ Power Regulator with Shutdown in SOT-23, 3V Output

Spice_Node_Sequence: [1] IN , [2] GND , [3] _SHDN , [4] BYP , [5] OUT ,

[LT1761-5.asy](#)

spice_model: LT1761-5

Required Refdes Prefix: X

spice_lib: LT1761.lib

Description: 100mA Low Noise Low Dropout μ Power Regulator with Shutdown in SOT-23, 5V Output

Spice_Node_Sequence: [1] IN , [2] GND , [3] _SHDN , [4] BYP , [5] OUT ,

[LT1761-BYP.asy](#)

spice_model: LT1761-BYP

Required Refdes Prefix: X

spice_lib: LT1761.lib

Description: 100mA Low Noise Low Dropout μ Power Regulator in SOT-23, Adjustable Output Voltage

Spice_Node_Sequence: [1] IN , [2] GND , [3] BYP , [4] ADJ , [5] OUT ,

[LT1761-SD.asy](#)

spice_model: LT1761-SD

Required Refdes Prefix: X

spice_lib: LT1761.lib

Description: 100mA Low Noise Low Dropout μ Power Regulator with Shutdown in SOT-23, Adjustable Output Voltage

Spice_Node_Sequence: [1] IN , [2] GND , [3] _SHDN , [4] ADJ , [5] OUT ,

[LT1762-1.5.asy](#)

spice_model: LT1762-1.5

Required Refdes Prefix: X

spice_lib: LT1762.lib

Description: 150mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 1.5V Output

Spice_Node_Sequence: [1] OUT , [2] SENSE , [3] BYP , [4] GND , [5] _SHDN , [6] IN ,

[LT1762-1.8.asy](#)

spice_model: LT1762-1.8

Required Refdes Prefix: X

spice_lib: LT1762.lib

Description: 150mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 1.8V Output

Spice_Node_Sequence: [1] OUT , [2] SENSE , [3] BYP , [4] GND , [5] _SHDN , [6] IN ,

[LT1762-2.5.asy](#)

spice_model: LT1762-2.5

Required Refdes Prefix: X

spice_lib: LT1762.lib

Description: 150mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 2.5V Output

Spice_Node_Sequence: [1] OUT , [2] SENSE , [3] BYP , [4] GND , [5] _SHDN , [6] IN ,

[LT1762-3.3.asy](#)

spice_model: LT1762-3.3

Required Refdes Prefix: X

spice_lib: LT1762.lib

Description: 150mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 3.3V Output

Spice_Node_Sequence: [1] OUT , [2] SENSE , [3] BYP , [4] GND , [5] _SHDN , [6] IN ,

[LT1762-3.asy](#)

spice_model: LT1762-3

Required Refdes Prefix: X

spice_lib: LT1762.lib

Description: 150mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 3V Output

Spice_Node_Sequence: [1] OUT , [2] SENSE , [3] BYP , [4] GND , [5] _SHDN , [6] IN ,

[LT1762-5.asy](#)

spice_model: LT1762-5

Required Refdes Prefix: X

spice_lib: LT1762.lib

Description: 150mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 5V Output

Spice_Node_Sequence: [1] OUT , [2] SENSE , [3] BYP , [4] GND , [5] _SHDN , [6] IN ,

[LT1762.asy](#)

spice_model: LT1762

Required Refdes Prefix: X

spice_lib: LT1762.lib

Description: 150mA Low Noise Low Dropout μ Power Regulator with Shutdown, Adjustable Output Voltage

Spice_Node_Sequence: [1] OUT , [2] ADJ , [3] BYP , [4] GND , [5] _SHDN , [6] IN ,

[LT1763-1.5.asy](#)

spice_model: LT1763-1.5

Required Refdes Prefix: X

spice_lib: LT1763.lib

Description: 500mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 1.5V Output

Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] BYP , [5] _SHDN , [6] IN ,

[LT1763-1.8.asy](#)

spice_model: LT1763-1.8

Required Refdes Prefix: X

spice_lib: LT1763.lib

Description: 500mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 1.8V Output

Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] BYP , [5] _SHDN , [6] IN ,

[LT1763-2.5.asy](#)

spice_model: LT1763-2.5

Required Refdes Prefix: X

spice_lib: LT1763.lib

Description: 500mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 2.5V Output

Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] BYP , [5] _SHDN , [6] IN ,

[LT1763-3.3.asy](#)

spice_model: LT1763-3.3

Required Refdes Prefix: X

spice_lib: LT1763.lib

Description: 500mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 3.3V Output

Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] BYP , [5] _SHDN , [6] IN ,

[LT1763-3.asy](#)

spice_model: LT1763-3

Required Refdes Prefix: X

spice_lib: LT1763.lib

Description: 500mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 3V Output

Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] BYP , [5] _SHDN , [6] IN ,

[LT1763-5.asy](#)

spice_model: LT1763-5

Required Refdes Prefix: X

spice_lib: LT1763.lib

Description: 500mA Low Noise Low Dropout μ Power Regulator with Shutdown, Fixed 5V Output

Spice_Node_Sequence: [1] OUT , [2] Sense , [3] GND , [4] BYP , [5] _SHDN , [6] IN ,

[LT1763.asy](#)

spice_model: LT1763

Required Refdes Prefix: X

spice_lib: LT1763.lib

Description: 500mA Low Noise Low Dropout μ Power Regulator with Shutdown, Adjustable Output Voltage

Spice_Node_Sequence: [1] OUT , [2] ADJ , [3] GND , [4] BYP , [5] _SHDN , [6] IN ,

[LT1764-1.5.asy](#)

spice_model: LT1764-1.5

Required Refdes Prefix: X

spice_lib: LT1764.lib

Description: 3A, Fast Transient Response, Low Noise, LDO Regulator, Fixed 1.5V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] Sense ,

[LT1764-1.8.asy](#)

spice_model: LT1764-1.8

Required Refdes Prefix: X

spice_lib: LT1764.lib

Description: 3A, Fast Transient Response, Low Noise, LDO Regulator, Fixed 1.8V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] Sense ,

[LT1764-2.5.asy](#)

spice_model: LT1764-2.5

Required Refdes Prefix: X

spice_lib: LT1764.lib

Description: 3A, Fast Transient Response, Low Noise, LDO Regulator, Fixed 2.5V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] Sense ,

[LT1764-3.3.asy](#)

spice_model: LT1764-3.3

Required Refdes Prefix: X

spice_lib: LT1764.lib

Description: 3A, Fast Transient Response, Low Noise, LDO Regulator, Fixed 3.3V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] Sense ,

[LT1764.asy](#)

spice_model: LT1764

Required Refdes Prefix: X

spice_lib: LT1764.lib

Description: 3A, Fast Transient Response, Low Noise, LDO Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] ADJ ,

[LT1764A-1.5.asy](#)

spice_model: LT1764A-1.5

Required Refdes Prefix: X

spice_lib: LT1764.lib

Description: 3A, Fast Transient Response, Low Noise, LDO Regulator, Fixed 1.5V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] Sense ,

[LT1764A-1.8.asy](#)

spice_model: LT1764A-1.8

Required Refdes Prefix: X

spice_lib: LT1764.lib

Description: 3A, Fast Transient Response, Low Noise, LDO Regulator, Fixed 1.8V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] Sense ,

[LT1764A-2.5.asy](#)

spice_model: LT1764A-2.5

Required Refdes Prefix: X

spice_lib: LT1764.lib

Description: 3A, Fast Transient Response, Low Noise, LDO Regulator, Fixed 2.5V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] Sense ,

[LT1764A-3.3.asy](#)

spice_model: LT1764A-3.3

Required Refdes Prefix: X

spice_lib: LT1764.lib

Description: 3A, Fast Transient Response, Low Noise, LDO Regulator, Fixed 3.3V Output

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] Sense ,

[LT1764A.asy](#)

spice_model: LT1764A

Required Refdes Prefix: X

spice_lib: LT1764.lib

Description: 3A, Fast Transient Response, Low Noise, LDO Regulator, Adjustable Output Voltage

Spice_Node_Sequence: [1] _SHDN , [2] IN , [3] GND , [4] OUT , [5] ADJ ,

[LT1765.asy](#)

spice_model: LT1765

Required Refdes Prefix: X

spice_lib: LT1765.sub

Description: Monolithic 3A, 1.25MHz Step-Down Switching Regulator

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] GND , [5] _SHDN , [6] FB , [7] Vc ,

[LT1766.asy](#)

spice_model: LT1766

Required Refdes Prefix: X

spice_lib: LT1766.sub

Description: High Voltage 200KHz Step-Down Switching Regulator

Spice_Node_Sequence: [1] GND , [2] SW , [3] Vin , [4] Boost , [5] BIAS , [6] Vc , [7] FB , [8] SYNC , [9] _SHDN ,

[LT1767-1.8.asy](#)

spice_model: LT1767-1.8

Required Refdes Prefix: X

spice_lib: LT1767-x.x.sub

Description: Monolithic 1.5A, 1.25MHz Step-Down Switching Regulator, Fixed 1.8V Output\n\nNote: Sync pin is not modeled.

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] GND , [5] _SHDN , [6] FB , [7] Vc ,

[LT1767-2.5.asy](#)

spice_model: LT1767-2.5

Required Refdes Prefix: X

spice_lib: LT1767-x.x.sub

Description: Monolithic 1.5A, 1.25MHz Step-Down Switching Regulator, Fixed 2.5V Output\n\nNote: Sync pin is not modeled.

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] GND , [5] _SHDN , [6] FB , [7] Vc ,

[LT1767-3.3.asy](#)

spice_model: LT1767-3.3

Required Refdes Prefix: X

spice_lib: LT1767-x.x.sub

Description: Monolithic 1.5A, 1.25MHz Step-Down Switching Regulator, Fixed 3.3V Output\n\nNote: Sync pin is not modeled.

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] GND , [5] _SHDN , [6] FB , [7] Vc ,

[LT1767-5.asy](#)

spice_model: LT1767-5

Required Refdes Prefix: X

spice_lib: LT1767-x.x.sub

Description: Monolithic 1.5A, 1.25MHz Step-Down Switching Regulator, Fixed 5V Output\n\nNote: Sync pin is not modeled.

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] GND , [5] _SHDN , [6] FB , [7] Vc ,

[LT1767.asy](#)

spice_model: LT1767

Required Refdes Prefix: X

spice_lib: LT1767.sub

Description: Monolithic 1.5A, 1.25MHz Step-Down Switching Regulator\n\nNote: Sync pin is not modeled.

Spice_Node_Sequence: [1] Boost , [2] Vin , [3] SW , [4] GND , [5] _SHDN , [6] FB , [7] Vc ,

[LT1776.asy](#)

spice_model: LT1776

Required Refdes Prefix: X

spice_lib: LT1776.sub

Description: Wide Input Range, High Efficiency, Step-Down Switching Regulator

Spice_Node_Sequence: [1] _SHDN , [2] Vcc , [3] SW , [4] GND , [5] Vin , [6] FB , [7] Vc ,

[LT1777.asy](#)

spice_model: LT1777

Required Refdes Prefix: X

spice_lib: LT1777.sub

Description: Low Noise Step-Down Switching Regulator

Spice_Node_Seque

[LT1009.asy](#)

spice_model: LT1009

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: 2.5V Adjustable Reference

Spice_Node_Sequence: [1] VCC , [2] VEE , [3] ADJ ,

[LT1021-10.asy](#)

spice_model: LT1021-10

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: Precision 10V Reference

Spice_Node_Sequence: [1] IN , [2] GND , [3] OUT , [4] TRIM ,

[LT1021-5.asy](#)

spice_model: LT1021-5

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: Precision 5V Reference

Spice_Node_Sequence: [1] IN , [2] GND , [3] OUT , [4] TRIM ,

[LT1021-7.asy](#)

spice_model: LT1021-7

Required Refdes Prefix: X

spice_lib: LTC3.lib

Description: Precision 7V Reference

Spice_Node_Sequence: [1] IN , [2] GND , [3] OUT ,

[capmeter.asy](#)

spice_model: capometer

Required Refdes Prefix: X

Description: A vector impedance meter. You must .lib capometer.sub

Spice_Node_Sequence: [1] DUT+ , [2] DUT- , [3] bias , [4] Resistance , [5] Capacitance ,

[LT1166.asy](#)

spice_model: LT1166

Required Refdes Prefix: X

spice_lib: LTC.lib

Description: Power Output Stage Automatic Bias System

Spice_Node_Sequence: [1] Vtop , [2] Vin , [3] Vout , [4] Vbottom , [5] Sense- , [6] Ilim- , [7] Ilim+ , [8] Sense+ ,

[LT1739.asy](#)

spice_model: LT1739

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Dual 500mA, 200MHz xDSL Line Driver Amplifier(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] S , [7] SR ,

[LT1794.asy](#)

spice_model: LT1794

Required Refdes Prefix: X

spice_lib: LTC1.lib

Description: Dual 500mA, 200MHz xDSL Line Driver Amplifier(1 of 2)

Spice_Node_Sequence: [1] In+ , [2] In- , [3] V+ , [4] V- , [5] OUT , [6] S , [7] SR ,

[LT4356-1.asy](#)

spice_model: LT4356-1
spice_lib: LT4356-1.sub
Description: Surge Stopper
Required Refdes Prefix: X
Spice_Node_Sequence: [1] TMR , [2] FB , [3] OUT , [4] Gate , [5] SNS , [6] Vcc , [7] _SHDN , [8] _FLT , [9] EN , [10] GND , [11] Aout , [12] IN+ ,
[LT4356-2.asy](#)

spice_model: LT4356-2
spice_lib: LT4356-2.sub
Description: Surge Stopper
Required Refdes Prefix: X
Spice_Node_Sequence: [1] TMR , [2] FB , [3] OUT , [4] Gate , [5] SNS , [6] Vcc , [7] _SHDN , [8] _FLT , [9] EN , [10] GND , [11] Aout , [12] IN+ ,
[LT4356-3.asy](#)

spice_model: LT4356-3
spice_lib: LT4356-3.sub
Description: Surge Stopper
Required Refdes Prefix: X
Spice_Node_Sequence: [1] TMR , [2] FB , [3] OUT , [4] Gate , [5] SNS , [6] Vcc , [7] _SHDN , [8] _FLT , [9] EN , [10] GND , [11] Aout , [12] IN+ ,
[LT6650.asy](#)

spice_model: LT6650
Required Refdes Prefix: X
spice_lib: LTC1.lib
Description: μ Power, 400mV Reference with Rail-to-Rail Buffer Amplifier in SOT-23
Spice_Node_Sequence: [1] IN , [2] GND , [3] OUT , [4] FB ,
[LTC1696.asy](#)

spice_model: LTC1696
Required Refdes Prefix: X
spice_lib: LTC1696.sub
Description: Overvoltage Protection Controller
Spice_Node_Sequence: [1] FB1 , [2] GND , [3] Vcc , [4] Out , [5] FB2 , [6] _Timer/Reset ,
[LTC1727-2.5.asy](#)

spice_model: LTC1727-2.5
Required Refdes Prefix: X
spice_lib: LTC1727-2.5.sub
Description: μ Power Precision Triple Supply Monitor in 8-Lead MSOP
Spice_Node_Sequence: [1] Vcc3 , [2] Vcc2.5 , [3] VccA , [4] GND , [5] CompA , [6] _RST , [7] Comp2.5 , [8] Comp3 ,
[LTC1727-5.asy](#)

spice_model: LTC1727-5
Required Refdes Prefix: X
spice_lib: LTC1727-5.sub
Description: μ Power Precision Triple Supply Monitor in 8-Lead MSOP
Spice_Node_Sequence: [1] Vcc3 , [2] Vcc5 , [3] VccA , [4] GND , [5] CompA , [6] _RST , [7] Comp5 , [8] Comp3 ,
[LTC1728-1.8.asy](#)

spice_model: LTC1728-1.8
Required Refdes Prefix: X
spice_lib: LTC1728-1.8.sub
Description: μ Power Precision Triple Supply Monitor in 5 lead SOT-23
Spice_Node_Sequence: [1] _RST , [2] GND , [3] VccA , [4] Vcc1.8 , [5] Vcc3 ,
[LTC1728-2.5.asy](#)

spice_model: LTC1728-2.5

Required Refdes Prefix: X

spice_lib: LTC1728-2.5.sub

Description: uPower Precidion Triple Supply Monitor in 5-lead SOT-23

Spice_Node_Sequence: [1] _RST , [2] GND , [3] VccA , [4] Vcc2.5 , [5] Vcc3 ,
[LTC1728-3.3.asy](#)

spice_model: LTC1728-3.3

Required Refdes Prefix: X

spice_lib: LTC1728-3.3.sub

Description: μ Power Precision Triple Supply Monitor in 5 lead SOT-23

Spice_Node_Sequence: [1] _RST , [2] GND , [3] VccA , [4] Vcc1.8 , [5] Vcc3 ,
[LTC1728-5.asy](#)

spice_model: LTC1728-5

Required Refdes Prefix: X

spice_lib: LTC1728-5.sub

Description: μ Power Precision Triple Supply Monitor in 5-lead SOT-23

Spice_Node_Sequence: [1] _RST , [2] GND , [3] VccA , [4] Vcc5 , [5] Vcc3 ,
[LTC1966.asy](#)

spice_model: LTC1966

Required Refdes Prefix: X

spice_lib: LTC1966.lib

Description: Precision μ Power, Delta-Sigma RMS-to-DC Converter

Spice_Node_Sequence: [1] GND , [2] In1 , [3] In2 , [4] Vss , [5] Vout , [6] OutRtn , [7] Vdd ,
[LTC1967.asy](#)

spice_model: LTC1967

Required Refdes Prefix: X

spice_lib: LTC1967.lib

Description: Precision Extended Bandwidth RMS-to-DC Converter

Spice_Node_Sequence: [1] GND , [2] In1 , [3] In2 , [4] Vout , [5] OutRtn , [6] Vdd ,
[LTC1968.asy](#)

spice_model: LTC1968

Required Refdes Prefix: X

spice_lib: LTC1968.lib

Description: Precision Wide Bandwidth RMS-to-DC Converter

Spice_Node_Sequence: [1] GND , [2] In1 , [3] In2 , [4] Vout , [5] OutRtn , [6] Vdd ,
[LTC2909-25.asy](#)

spice_model: LTC2909-25

Required Refdes Prefix: X

Description: Dual Adjustable UV/OV Monitor, 2.5V UVLO

spice_lib: LTC2909-25.sub

Spice_Node_Sequence: [1] GND , [2] REF , [3] ADJ2 , [4] ADJ1 , [5] SEL , [6] TMR , [7] Vcc , [8] _RST ,

[LTC2909-33.asy](#)

spice_model: LTC2909-33

Required Refdes Prefix: X

Description: Dual Adjustable UV/OV Monitor, 3.3V UVLO

spice_lib: LTC2909-33.sub

Spice_Node_Sequence: [1] GND , [2] REF , [3] ADJ2 , [4] ADJ1 , [5] SEL , [6] TMR , [7] Vcc , [8] _RST ,

[LTC2909-50.asy](#)

spice_model: LTC2909-50

Required Refdes Prefix: X

Description: Dual Adjustable UV/OV Monitor, 5.0V UVLO

spice_lib: LTC2909-50.sub

Spice_Node_Sequence: [1] GND , [2] REF , [3] ADJ2 , [4] ADJ1 , [5] SEL , [6] TMR , [7] Vcc , [8] _RST ,

[LTC2952.asy](#)

spice_model: LTC2952

Required Refdes Prefix: X

Description: Dual Ideal Diode Push Button Power Path Controller with Supervisor

spice_lib: LTC2952

Spice_Node_Sequence: [1] VM , [2] PFI , [3] WDE , [4] _PB , [5] _RST , [6] _PFO , [7] ONT , [8] OFFT , [9] GND , [10] _INT , [11] EN , [12] G2 , [13] V2 , [14] VS , [15] V1 , [16] G1 , [17] G1Stat , [18] M2 , [19] M1 , [20] _KILL ,

[LTC2954-1.asy](#)

spice_model: LTC2954-1

Required Refdes Prefix: X

spice_lib: LTC2954-1.sub

Description: Push Button On/Off Controller with μ P Interrupt

Spice_Node_Sequence: [1] Vin , [2] _PB , [3] ONT , [4] GND , [5] _INT , [6] EN , [7] PDT , [8] _KILL ,

[LTC2954-2.asy](#)

spice_model: LTC2954-2

Required Refdes Prefix: X

spice_lib: LTC2954-2.sub

Description: Push Button On/Off Controller with μ P Interrupt

Spice_Node_Sequence: [1] Vin , [2] _PB , [3] ONT , [4] GND , [5] _INT , [6] _EN , [7] PDT , [8] _KILL ,

[LTC4265.asy](#)

spice_model: LTC4265

Required Refdes Prefix: X

spice_lib: LTC4265.sub

Description: IEEE802.3at High Power PD Interface Controller with 2-event Classification

Spice_Node_Sequence: [1] SHDN , [2] _T2PSE , [3] RCLASS , [4] Vin , [5] Vout , [6] PWRGD , [7] _PWRGD , [8] AGND ,

[LTC4300A-1.asy](#)

spice_model: LTC4300A-1

Required Refdes Prefix: X

spice_lib: LTC4300A-1.sub

Description: Hot Swappable 2-Wire Bus Buffers Note: Stop Bit and Bus Idle detection not modeled

Spice_Node_Sequence: [1] ENABLE , [2] SCLOUT , [3] SCLIN , [4] GND , [5] READY , [6] SDAIN , [7] SDAOUT , [8] Vcc ,

[LTC4352.asy](#)

spice_model: LTC4352

spice_lib: LTC4352.sub

Description: Low Voltage Ideal Diode Controller with Monitoring

Required Refdes Prefix: X

Spice_Node_Sequence: [1] Vin , [2] Vcc , [3] UV , [4] OV , [5] _Status , [6] _Fault , [7] Rev , [8] Out , [9] GND , [10] CPO , [11] Gate , [12] Source ,

[LTC4355.asy](#)

spice_model: LTC4355

Required Refdes Prefix: X

spice_lib: LTC4355

Description: Positive High Voltage Ideal Diode-OR with Input Supply and Fuse Monitors

Spice_Node_Sequence: [1] Mon1 , [2] Mon2 , [3] GND , [4] Set , [5] _FuseFlt2 , [6] _FuseFlt1 , [7] _VdsFlt , [8] Out , [9] Gate2 , [10] In2 , [11] Gate1 , [12] In1 , [13] _PwrFlt1 , [14] _PwrFlt2 ,

[LTC4357.asy](#)

spice_model: LTC4357

Required Refdes Prefix: X

spice_lib: LTC4357.sub

Description: Positive High Voltage Ideal Diode Controller

Spice_Node_Sequence: [1] OUT , [2] IN , [3] Gate , [4] GND , [5] Vdd ,

[LTC4358.asy](#)

spice_model: LTC4358

Required Refdes Prefix: X

spice_lib: LTC4358.sub

Description: 5A Ideal Diode

Spice_Node_Sequence: [1] OUT , [2] IN , [3] Drain , [4] Gate , [5] Vdd , [6] GND ,

[LTC4360-1.asy](#)

spice_model: LTC4360-1

Required Refdes Prefix: X

spice_lib: LTC4360-1.sub

Description: Overvoltage Protection Controller

Spice_Node_Sequence: [1] In , [2] Gate , [3] _On , [4] Out , [5] _Pwrgd , [6] Gnd ,

[LTC4360-2.asy](#)

spice_model: LTC4360-2

Required Refdes Prefix: X

spice_lib: LTC4360-2.sub

Description: Overvoltage Protection Controller

Spice_Node_Sequence: [1] In , [2] Gate , [3] Gp , [4] Out , [5] _Pwrgd , [6] Gnd ,

[LTC4361-1.asy](#)

spice_model: LTC4361-1

Required Refdes Prefix: X

spice_lib: LTC4361-1.sub

Description: Overvoltage/Overcurrent Protection Controller

Spice_Node_Sequence: [1] GP , [2] In , [3] Sense , [4] Gate , [5] _On , [6] Out , [7] _Pwrgd , [8] Gnd ,

[LTC4361-2.asy](#)

spice_model: LTC4361-2

Required Refdes Prefix: X

spice_lib: LTC4361-2.sub

Description: Overvoltage/Overcurrent Protection Controller

Spice_Node_Sequence: [1] GP , [2] In , [3] Sense , [4] Gate , [5] _On , [6] Out , [7] _Pwrgd , [8] Gnd ,

[LTC4362-1.asy](#)

spice_model: LTC4362-1

Required Refdes Prefix: X

spice_lib: LTC4362-1.sub

Description: 1.2A Overvoltage/Overcurrent Protector

Spice_Node_Sequence: [1] GP , [2] In , [3] Sense , [4] Out , [5] _On , [6] _Pwrgd , [7] Gnd ,

[LTC4362-2.asy](#)

spice_model: LTC4362-2

Required Refdes Prefix: X

spice_lib: LTC4362-2.sub

Description: 1.2A Overvoltage/Overcurrent Protector

Spice_Node_Sequence: [1] GP , [2] In , [3] Sense , [4] Out , [5] _On , [6] _Pwrgd , [7] Gnd ,

[LTC4411.asy](#)

spice_model: LTC4411

Required Refdes Prefix: X

spice_lib: LTC4411.sub

Description: 2.6A Low Loss Ideal diode in ThinSOT

Spice_Node_Sequence: [1] Vin , [2] GND , [3] CTL , [4] STAT , [5] OUT ,

[LTC4413-1.asy](#)

spice_model: LTC4413-1
Required Refdes Prefix: X
spice_lib: LTC4413-1.sub
Description: Dual 2.6A, 2.5V to 5.5V Fast Ideal Diodes in 3mm×3mm DFN
Spice_Node_Sequence: [1] INA , [2] ENBA , [3] GND , [4] ENBB , [5] INB , [6] OUTB , [7] STAT , [8] OUTA ,
[LTC4413-2.asy](#)
spice_model: LTC4413-2
Required Refdes Prefix: X
spice_lib: LTC4413-2.sub
Description: Dual 2.6A, 2.5V to 5.5V Fast Ideal Diodes in 3mm×3mm DFN
Spice_Node_Sequence: [1] INA , [2] ENBA , [3] GND , [4] ENBB , [5] INB , [6] OUTB , [7] OVP , [8] OVI , [9] STAT , [10] OUTA ,
[LTC4413.asy](#)
spice_model: LTC4413
Required Refdes Prefix: X
spice_lib: LTC4413.sub
Description: Dual 2.6A, 2.5V to 5.5V Ideal Diodes in 3mm×3mm DFN
Spice_Node_Sequence: [1] INA , [2] ENBA , [3] GND , [4] ENBB , [5] INB , [6] OUTB , [7] STAT , [8] OUTA ,
[LTC4414.asy](#)
spice_model: LTC4414
Required Refdes Prefix: X
spice_lib: LTC4414.sub
Description: 36V, Low Loss PowerPath Controller for Large PFETs
Spice_Node_Sequence: [1] STAT , [2] CTL , [3] GND , [4] Sense , [5] Vin , [6] Gate ,
[LTC4416-1.asy](#)
spice_model: LTC4416-1
Required Refdes Prefix: X
spice_lib: LTC4416-1.sub
Description: 36V, Low Loss Dual PowerPath Controller for Large PFETs
Spice_Node_Sequence: [1] H1 , [2] E1 , [3] GND , [4] _E2 , [5] _H2 , [6] G2 , [7] V2 , [8] Vs , [9] V1 , [10] G1 ,
[LTC4416.asy](#)
spice_model: LTC4416
Required Refdes Prefix: X
spice_lib: LTC4416.sub
Description: 36V, Low Loss Dual PowerPath Controller for Large PFETs
Spice_Node_Sequence: [1] H1 , [2] E1 , [3] GND , [4] _E2 , [5] _H2 , [6] G2 , [7] V2 , [8] Vs , [9] V1 , [10] G1 ,
[LTC5505-1.asy](#)
spice_model: LTC5505-1
Required Refdes Prefix: X
spice_lib: LTC5505-1.sub
Description: RF Power Detector with Buffered Output and >40dB Dynamic Range
Spice_Node_Sequence: [1] RFin , [2] GND , [3] _SHDN , [4] Vout , [5] Vcc ,
[LTC5505-2.asy](#)
spice_model: LTC5505-2
Required Refdes Prefix: X
spice_lib: LTC5505-2.sub
Description: RF Power Detector with Buffered Output and >40dB Dynamic Range
Spice_Node_Sequence: [1] RFin , [2] GND , [3] _SHDN , [4] Vout , [5] Vcc ,
[LTC6900.asy](#)

spice_model: LTC6900
Required Refdes Prefix: X
spice_lib: LTC6900.sub
Description: Resistor Set SOT-23 Oscillator
Spice_Node_Sequence: [1] V+ , [2] GND , [3] SET , [4] DIV , [5] OUT ,
[LTC6908-1.asy](#)
spice_model: LTC6908-1
Required Refdes Prefix: X
spice_lib: LTC6908-1.sub
Description: Resistor Set SOT-23 Oscillator with Spread Spectrum Modulation
Spice_Node_Sequence: [1] V+ , [2] GND , [3] SET , [4] MOD , [5] OUT2 , [6] OUT1 ,
[LTC6908-2.asy](#)
spice_model: LTC6908-2
Required Refdes Prefix: X
spice_lib: LTC6908-2.sub
Description: Resistor Set SOT-23 Oscillator with Spread Spectrum Modulation
Spice_Node_Sequence: [1] V+ , [2] GND , [3] SET , [4] MOD , [5] OUT2 , [6] OUT1 ,
[LTC6990.asy](#)
spice_model: LTC6990
Required Refdes Prefix: X
spice_lib: LTC6990.sub
Description: TimerBlox SOT-23 VCO
Spice_Node_Sequence: [1] OE , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,
[LTC6991.asy](#)
spice_model: LTC6991
Required Refdes Prefix: X
spice_lib: LTC6991.sub
Description: TimerBlox Low Freq Clock
Spice_Node_Sequence: [1] RST , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,
[LTC6992-1.asy](#)
spice_model: LTC6992-1
Required Refdes Prefix: X
spice_lib: LTC6992-1.sub
Description: TimerBlox 0% to 100% DC PWM
Spice_Node_Sequence: [1] MOD , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,
[LTC6992-2.asy](#)
spice_model: LTC6992-2
Required Refdes Prefix: X
spice_lib: LTC6992-2.sub
Description: TimerBlox 5% TO 95% PWM
Spice_Node_Sequence: [1] MOD , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,
[LTC6992-3.asy](#)
spice_model: LTC6992-3
Required Refdes Prefix: X
spice_lib: LTC6992-3.sub
Description: TimerBlox 0% TO 95% PWM
Spice_Node_Sequence: [1] MOD , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,
[LTC6992-4.asy](#)
spice_model: LTC6992-4
Required Refdes Prefix: X
spice_lib: LTC6992-4.sub
Description: TimerBlox 5% TO 100% PWM
Spice_Node_Sequence: [1] MOD , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[LTC6993-1.asy](#)

spice_model: LTC6993-1

Required Refdes Prefix: X

spice_lib: LTC6993-1.sub

Description: TimerBlox Non-Retriggerable One Shot

Spice_Node_Sequence: [1] TRIG , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[LTC6993-2.asy](#)

spice_model: LTC6993-2

Required Refdes Prefix: X

spice_lib: LTC6993-2.sub

Description: TimerBlox Retriggerable One Shot

Spice_Node_Sequence: [1] TRIG , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[LTC6993-3.asy](#)

spice_model: LTC6993-3

Required Refdes Prefix: X

spice_lib: LTC6993-3.sub

Description: TimerBlox Non-Retrig Fall Edge One Shot

Spice_Node_Sequence: [1] TRIG , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[LTC6993-4.asy](#)

spice_model: LTC6993-4

Required Refdes Prefix: X

spice_lib: LTC6993-4.sub

Description: TimerBlox Retriggerable One Shot

Spice_Node_Sequence: [1] TRIG , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[LTC6994-1.asy](#)

spice_model: LTC6994-1

Required Refdes Prefix: X

spice_lib: LTC6994-1.sub

Description: TimerBlox Retriggerable One Edge Delay

Spice_Node_Sequence: [1] IN , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[LTC6994-2.asy](#)

spice_model: LTC6994-2

Required Refdes Prefix: X

spice_lib: LTC6994-2.sub

Description: TimerBlox Retriggerable Either Edge Delay

Spice_Node_Sequence: [1] IN , [2] GND , [3] SET , [4] DIV , [5] V+ , [6] OUT ,

[modulate.asy](#)

Required Refdes Prefix: A

spice_lib: MODULATOR

Description: Behavioral VCO and Modulator. Output is a sine wave modulated by AM and FM input voltages.

Spice_Node_Sequence: [1] FM , [2] AM , [3] Q , [4] com ,

[modulate2.asy](#)

Required Refdes Prefix: A

spice_lib: MODULATOR

Description: Behavioral VCO and Modulator. Output is a sine wave modulated by AM and FM input voltages. This version supplies an output in quadrature.

Spice_Node_Sequence: [1] FM , [2] AM , [3] COS , [4] SIN , [5] com ,

[sample.asy](#)

Required Refdes Prefix: A

spice_lib: SAMPLEHOLD

Description: Behavioral Sample and Hold function block

Spice_Node_Sequence: [1] in+ , [2] in- , [3] CLK , [4] S/H , [5] out , [6] com ,

[varistor.asy](#)

Required Refdes Prefix: A

spice_lib: VARISTOR

Description: Voltage controlled varistor

Spice_Node_Sequence: [1] invin , [2] noninvin , [3] out , [4] com ,